Faculty Profile

1. Name: Mr.Nagesh P Patil

2. Designation: Assistant Professor, Department of Computer Engg, SIT-Lonavala

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4. Mobile: +91-7276163418
5. Date of Birth: 30th July. 1982

6. Address: Kapoor Building, Building No. 4 Flat No.12, STES Campus, Lonavala

7. Research Interests: Network security, Machine Learning, Database security.

Educational Details: .

- M.Tech.(Computer Science & Engineering), JNTU, Hyderabad, First class with Distinction(71.40%), 2010
- B.E. (Information Technology), Shivaji University, Kolhapur, First class (66.36 %), 2006
- Diploma in Computer Engineering, MSBTE, Mumbai, First class (61.37%), 2003
- S.S.C (Pune Board), Jagruti Vidya Mandir, Solapur First class (71.46%), 1998

Experience: (16 years)

- o Jan 2025 to Till date: Assistant Professor, Computer Dept, at SIT, Lonavala, India
- o Nov 2013 to Jan 2025: **Principal,** DPSS's Balasaheb Mhatre Polytechnic, India
- July 2013 to Nov 2013: Principal, SKET's Dr. Manoj A Shete college of Engineering & Technology Kasara, India.
- o Feb 2012 to 2013: **HOD** Computer Dept, at GES's Katagara Polytechnic Institute, Bordi, India
- Aug 2011 to Feb 2012: Principal, SKET's Dr. Manoj A Shete college of Engineering & Technology Kasara, India.
- 2010 to 2011:- Assistant Professor in Computer Engg. Dept. at Alamuri Ratnamala Institute of Engineering & Technology, Shahapur, India
- 2008 to 2010:- Lecturer in Computer Engg. Dept at Secab Institute of Engineering and Technology, Bijapur, India

Subjects Taught:

Java Programming
Design Logic & Application Development

• Microprocessor Web Technology

• Software Engineering Principles of Programming Language

Discrete Mathematics

Responsibilities Handled:

- ✓ College Administration
- ✓ Committee Member- MSBTE EIMC
- ✓ External Examiner for Final Year Projects
- ✓ Controller of Examination
- ✓ Chief Officer In charge of EC

Research publications:

Place: Lonavala

• International Journals: 06

FDP, STTP/ WS/ Webinar/GL/ Conference Conducted

• One week FDP on "CRYPTOCURRENCY"

Mr.Nagesh P Patil