# Prof. SHEETAL H.BARSHIKAR

**Assistant Professor, Department of E & TC**

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## OVERVIEW:

**Qualifications:** ME (Electronics), BE (E&TC)
**Experience:** Teaching -16 years Tech Assist – 4.5 years
**Date of Joining STES:** 03/12/2003
**Skills and Proficiencies: Embedded C,** ISE Design Suit Simulator 14.7(VHDL Program), Keil 5,Scilab,C

## SIGNIFICANT ACHIEVEMENTS:

Techtonic 24 Memento Co-ordinator Appreciation certificate, SPPU Examiner,Moderator,Cusrow Wadiya Paper Setter,Student activity committee member,Purchase Committee Member,Tech Tonic 24_memento_central Coordinator,SC_ST_Central_commitee member

## AREA OF EXPERTISE /RESEARCH INTERESTS:

- Embedded System ,VHDL Design and Technology,

## NUMBER OF STUDENTS GUIDED:

- UG – 12 Students

## RESEARCH PULICATIONS:

**Publication Summary:**

Scopus/ SCI-NA
International Journal: 02
Conference : 04
h-index : NA
i10 index : NA

## RESEARCH WORK:

**PhD Research Work:**

- The main objective of the research work is “Machine Learning Techniques for Detection and Classification of Kidney Tumor”. .

- The research is under the guidance of Dr.S.S.Lokhande madam from Sinhgad College of Engineering**, Pune**

## NOTABLE GUIDED PROJECTS:

**UG projects:**

- **Replacement of Brakevan with EOTT System(Year 2023-24)**
  The Research unveils the design and implementation of an Arduino powered EOTT system.The track portion of the system consists of Arduino Nano, metal detectors,nRF modules and LEDs,while the train unit uses Arduino Nano Controllers,GPS,GSM,nRF modules,a buzzer, and a color sensor. The EOTT system leverages wireless connectivity,GSM communication and real time GPS tracking to facilitate data sharing between train and track units with the goal of enhancing safety and efficiency.

## SUBJECTS TAUGHT:

- Computer Fundamentals
- Electronic Components and Applications
- Digital Electronics
- Microprocessor Techniques
- Basic Electronics
- Computer Hardware and Networking
- Digital Electronics and Logic Design
- Processor Architecture
- Social Media Analytics
- Microcontrollers
- VLSI Design and Technology
- Mobile Communication
- Broadband Communication System
- Advanced Processors
- Cellular Network

## FDP/STTP/SDP ATTENDED:

- TEQIP/AICTE/ISTE/IETE sponsored FDP -08

## RESPONSIBILITIES HANDLED AT STES/SCOE/DEPT:

**Institute Level (SCOE)**

- Worked as a Memento Coordinator of the Institute Techtonic Feb 2024
- Worked as a Discipline committee member for Dandiya event AY 2023-24.
- Admission Counselling Cell

**Department Level (E&TC)**

- Teacher Guardian, Class Teacher
- Organizing committee member for NCPC 24 in April 24
- Organizing committee member for One Week STTP “AI Integration of Energy Efficient Architectures in VLSI Design
- Project Guide for BE students
- NBA Criterion 4 member
- Departmental Purchase Committee member
- Departmental Student Certificate committee member

- BE Audit Course Coordinator-MIS-Sem 1(2024-25)
- Internship Faculty Mentor
- Worked as a Discipline committee member for BE Farewell Event AY 2023-24(Sem 2)

**DECLARATION:**

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date: / / 2024 Signature