




Prof. Rupam Bhaskar Nawale

Assistant Professor, Department of E & TC

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OVERVIEW:

Qualifications: ME (Embedded & VLSI), BE (Ed  Engg)
Experience: Teaching -2.6 years
Date of Joining STES: 11/04/2022
Skills and Proficiencies: SQL, HTML, C, ASP.NET

AREA OF EXPERTISE /RESEARCH INTERESTS:

- VLSI
- Embedded system
- Digital Electronics

RESEARCH WORK:

- Developed 3G repeaters essential for enhancing and extending mobile network coverage in areas with weak signals. These repeaters support various frequency bands, provide optimal gain, maintain a low noise figure, ensure adequate isolation, and meet power requirements. This is crucial for achieving optimal performance and compliance with regulatory standards.

NOTABLE GUIDED PROJECTS:

- Arduino Voice Controlled Robot

RESOURCE PERSON TO INDUSTRY/ACADEMIA:

- Resource Person at “Train The Trainer” program on the topic “Design and Implementation of Multiplexer and Demultiplexer”, E&TC Dept. ,SCOE

SUBJECTS TAUGHT:

- Microprocessor 80386
- Digital Electronics and Logic Design
- Project Based Learning

FDP/STTP/SDP/WORKSHOPS ORGANIZED AS A COORDINATOR:

- Committee Member of ISTE sponsored one-week online STTP titled Recent Advances in Deep Learning and its multidisciplinary approach, Sinhgad College of Engineering, Pune, 23-28 January 2023.
- Committee Member of ISTE sponsored one-week online STTP titled AI Integration of Energy Efficient Architectures in VLSI Design, Sinhgad College of Engineering, Pune, 05-09 August 2024.
- Organizing Committee Member of National Conference on Pervasive Computing (NCPC), Sinhgad College of Engineering, Pune, 05th April, 2024.
- Organizing Committee Member of one-week FDP on Recent Trends in Artificial Intelligence & Data Science Sinhgad College of Engineering, Pune, 24-28 July 2023.

CONFERENCES ATTENDED:

- Attended 2015 e-PGCON, at AISSMS Pune, India. June 2015.
- Attended 2024 National Conference on Pervasive Computing at SCOE , Pune. 5th April 2024

FDP/STTP/SDP ATTENDED:

- ISTE/IETE Approved STTP- 03
- AICTE Approved FDP- 01
- FDP- 07
- FOW- 03

RESPONSIBILITIES HANDLED AT STES/SCOE/DEPT:

- **Institute Level (SCOE)**
 - Working in NEON as a committee member (2024)
- **Department Level (E&TC)**
 - Lab Incharge for Microprocessor Laboratory, TeacherGuardian, Subject In charge
 - Organizing committee member for various FacultyDevelopment Program
 - Departmental Result Analysis Committee Co-coordinator
 - Departmental Competitive Exam Coordinator
- Departmental Result analysis committee member
 - Departmental T&P committee member
 - Departmental PR/OR committee member
 - NBA committee member
 - Departmental Higher Study Coordinator

DECLARATION:

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date: / / 2024

Signature

