



Prof. R. A. VASMATKAR
Assistant Professor, Department of Computer Engineering



OVERVIEW:

Qualification: M.Tech (VLSI & Embedded Systems), BE (E & Tc)
Experience: Teaching 9.5 Years
Date of Joining STES: 04 / 07 /2022
Skills and Proficiencies: VLSI, VHDL, Microprocessor, Microcontroller, Computer Networking and Security.

AREA OF EXPERTISE /RESEARCH INTERESTS:

- VLSI Design and ES, Wireless Communication

NUMBER OF STUDENTS GUIDED:

- UG – 12 Groups

NOTABLE GUIDED PROJECTS:

- Shopping in Metaverse

RESEARCH PUBLICATIONS:

Publication Summary:

International Journals - 09

Citations- 5

index - 1

<https://scholar.google.co.in/citations?user=6gJAFIAAAAJ&hl=en&oi=sra>

SUBJECTS TAUGHT:

- Computer Networks and Security Systems,
- Microprocessor,
- HSS
- Digital Electronics
- Information Theory & Coding Techniques
- Computer Networks & Security
- High Performance Communication & Networking
- Optical Fiber Communication
- Humanity and Social Science
- Digital Communication
- Operating Systems
- VLSI Technology

FDP/STTP/SDP/ATTENDED:

- Workshop on “Broad Band Communication” during 28/04/2006 to 29/04/2006 at REC Bhalki, Karnataka.
- Refresher Course on” HDL” during 27/01/10 to 29/01/10 at REC Bhalki, Karnataka.
- STTP on “Advanced VLSI and Microelectronics Design” during 28/02/2011 to 02/03/2011 at MIT College of Engineering, Pune.
- Workshop on “Measurement Techniques in Optical and RF Communication” during 09/01/2012 to 10/01/2012 at MES College of Engineering, Pune.
- National Level Faculty Development Programme on Problem Solving & Ideation as 21st Century Skills on Thursday February 10, 2022 at MIT School of Engineering, Loni kalbhor, Pune.
- Co-ordinator for AICTE approved event - Poster Exhibition On Prevention of Sexual Harassment of Women at workplace”, organized on 5th December 2022, by Computer Engineering Department, Sinhgad College of Engineering.
- Faculty Orientation Program on “Image Processing” on 20/01/23, held online by PICT, Pune.
- Five days STTP approved by AICTE on - “Emerging Technological Advancements And Practical Applications of Data Science” during 16/01/23 to 20/01/23 Organized by SKN Sinhgad Institute of Technology and Science, Lonavala.
- FDP on “Holistic Development through NEP 2020”, International FDP on “AI for sustainable development.
- Participated in the AICTE Recognized Faculty Development Programme on “Outcome based Curriculum Development” Conducted by Curriculum Development Centre Department from 11/09/2023 to 15/09/2023 (One Week)
- Participated in International Faculty Development Program on “ AI for sustainable Development” from 5th Aug 2024 to 9th Aug 2024, organized by VIIT, Pune, IEEE Region 10 Humanitarian Technology activities, IEEE Pune section, IEEE Pune Section SIGHT group, IEEE Pune section SIG on“Energy and Environmental Engineering” and IEEE VIIT Student Branch.

RESPONSIBILITIES HANDLED AT STES/SCOE/DEPT:**1.Department Level (Computer)**

- Lab In charge for Computer Network Laboratory
- Teacher Guardian
- Class Teacher
- Project Guide for BE students
- NBA committee member
- Departmental Techtonic Event Coordinator (Event Poster making Competition)

DECLARATION:

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date: / / 2024

Signature