

Faculty Profile

1. Name: **Mrs. P. S. Mhetre**
2. Designation: Asst. Professor, ETC department,
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5. Date of Birth: 23/06/1989
6. Address: Flat No. 11, New 2BHK Building, STES campus Lonavala, Kusgaon B.K.
7. Research Interests: Deep Learning, Image Processing , Embedded System.



Educational Details:

- **M.E.** (VLSI & Embedded), from SAE kondhava, Pune, First class with distinction, 2012.
- **B.E.** (Electronics and Telecommunication) from SVPM'S College of Engineering, baramati, Pune, First class, 2010.
- **H.S.C.** from T.C. College, Baramati, First class, 2006.

Experience: (10 years)

- 2014 till date: - Working as a Asst. Professor in E&TC Dept, Sinhgad Institute of Technology, Lonavala, India.

Responsibilities:

- Working as a Asst. Professor from 2014.

Research publications:

- International Journals: **04**, National Journal: **01**

Achievements and Awards:

- Attended two week online Short Term Training Program (STTP) on "Mastering Research Writing & Intellectual Property : A Comprehensive Approach to Academic Success" approved by ISTE
- Worked as session chair at IEEE TECHNICOKNOCKDOWN - 2023 (TKD-23).
- Worked as Track chair in International conference 'GCWCN', at SIT Lonaval.
- Done Review of papers in IEEE GCWCN2018.
- Attended FDP ON DATA SCIENCE held on 8th feb to 13th feb2021
- Attended One Week Webinar Series IT - Business Applications On web-Bizz 2020
- Attended the Webinar on "Digi-Tech Teaching Learning- 2020", organized by, Sinhgad Institute of Technology, Lonavala, India, on MAY, 18th , 2020.
- Participated and successfully completed FDP101x and FDP201x organized by IIT Bombay.

Place

Lonavala

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