

Faculty Profile

1. Name: Mrs. Sanika Sagar Bhardwaj
2. Designation: Assistant Professor in E&TC Department, SIT,Lonavala
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6. Address: STES's Staff Quarters, Kapoor-III, Flat No.5, Kusgaon(Bk), Lonavala
7. Research Interests: **Image Processing, Analog Electronics, VLSI**



Educational Details:

- Ph.D. pursuing at Bits Pilani, K.K. Birla, Goa Campus, EEE department.
- M. E. in Electronics & Telecommunication, Bharati Vidyapeeth, Pune
- Degree: B.E. in Electronics, Walchand College of Engineering, Sangli
- H.S.C. & S.S.C, Maharashtra State Board

Experience: (17.5 years)

- 2007 till date:- Working as a **Assistant Professor** in E/TC Dept, Sinhgad Institute of Technology, Lonavala, India.

Responsibilities:

- Worked as a Technic Departmental Co-ordinator
- Worked as an Examination Senior Supervisor
- Worked as a Lab Innovation Co-ordinator
- NBA Decoration Committee Head
- Worked as a Lab In charge and Class incharge

Research publications:

- International Journals: **10**, International Conference: **07**, National Conference: **07**

Professional Membership:

- Life Member: Indian Society for Technical Education (LM53535)
- IETE

Achievements and Awards:

Place: Lonavala

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