


- **Faculty Profile**

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<b>Qualification</b>	Ph.D (Pursuing) M E (VLSI & Embedded System)	
<b>Experience</b>	Teaching- 12 Yrs., Industry- 0 Yrs.	
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<b>Publications</b>	<a href="https://scholar.google.com/citations?user=wiROGZAAAAAJ&amp;hl=en">https://scholar.google.com/citations?user=wiROGZAAAAAJ&amp;hl=en</a>	
<b>Patent granted/published</b>	A device for improving CASCODE current mirror using voltage reduction	