


Faculty Profile

Name	Dr. Dharmendra G. Ganage	
Qualification	Ph. D. (Electronics & Telecommunication Engineering)	
Experience	Teaching- 17 Yrs.	
Date of Joining STES	23/07/2008	
email id	dgganage.scoe@sinhgad.edu	
No. of students guided	Ph D – 00; M.E. - 10	
Publications	https://scholar.google.co.in/citations?user=YzfUe60AAAAJ&hl=en	
Patent granted/published	Patent granted on IoT based air quality sensor device, Government of India, Date of Issue: 14/03/2023 (Published - Journal No is 11/2023 and Journal Date is 17/03/2023)	
Consultancy Projects:	<ol style="list-style-type: none"> 1. Raspberry pi-based surveillance security system. 2. Contactless attendance management system using AI 	
Significant Achievements:	<ol style="list-style-type: none"> 1. Recognition as a Research Supervisor (PhD guide) of SPPU, Pune, Nov. 7, 2023 2. Golden Reviewers for 2020: IETE Journal of Research, Mar. 4, 2021. 3. Award for Appreciation – Organizing one week FDP on Recent Trends in Communication Engineering during Aug. 23-27, 2021, by IEEE Pune section and IEEE Communication Society. 4. Certificate of Appreciation as “Alumni Chief Coordinator” of Sinhgad College of Engineering for successfully organizing Alumni Meet- 2020 held on 29th Feb. 2020. 5. Certificate of Appreciation as “Alumni Chief Coordinator” of Sinhgad College of Engineering for successfully organizing Alumni Meet- 2014 held on 21st June 2014. 6. Book Published - Megha Wankhade, Yugendra Chincholkar, Dharmendra Ganage, Suchita Wagh-Ganage, “Electronic Devices and Circuits”, Scientific International Publishing House, India, ISBN: 978-93-5757-463-1. 	