Faculty Profile

Name	Dr Yugendra Devidas Chincholkar
Qualification	Ph D (Electronics & Telecommunication)
Experience	Teaching- 21 Yrs., Industry- 1 Yrs.
Date of Joining STES	07/08/2008
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No. of students guided	Ph D – 00; M.E. - 10
Publications	https://scholar.google.com/citations?user=zD6m51AAAAAJ&hl=en
Patent granted/published	1. IoT-based Air Quality Sensor Device (Published- ID 358781-101) 2. E-Scooter Wireless Charging Station (Published- ID 356611-001)
Funded Research Projects Details: *(Number)*	1. **Project Title:** MODROBS for Modernization for **Communication Network Lab** **Amount Received:** 6,00,000/- **Duration:** 2011 to 2012 **Funding Agency:** AICTE, New Delhi. 2. **Project Title:** Design & performance evaluation of patchwork coding-based audio watermarking for signal processing attack **Amount Received:** 1,20,000/- **Duration:** 2016 to 2018 **Funding Agency:** BCUD, SPPU Pune **Publication:** 1. Yugendra Chincholkar and Sanjay Ganorkar, **"Audio Watermarking Algorithm Implementation using Patchwork Technique"** 2019 5th IEEE International Conference for Convergence in Technology (I2CT) Pune, India. pp- Mar 29-31, 2019. 2. Y. D. Chincholkar, Dr. S. R. Ganorkar, Shalaka Pravin Kude **"A Survey: Digital Audio Watermark Designed Methods"** *International Journal of Advanced Research in Computer and Communication Engineering (**IJARCCE**),* Volume 6, Issue 6, June 2017, pp-288-292.