



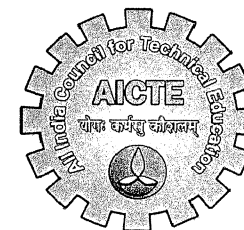
Sinhgad Institutes

Celebrating 25 Years

STES'S

Smt. Kashibai Navale College of Engineering

S.No. 44/1, Vadgaon (Bk), Off Sinhgad Road
Pune - 411 041, Maharashtra, India



Department of Electronics & Telecommunication Engineering

Certificate

This is to certify that

Mrs / Ms. Vaishali M. Sardeshmukh

of Institute / Organization SAE, Kondhawa, Pune.

Participated in Two Week AICTE sponsored Faculty Development Program

On

"Ethics and Moral Values"

Conducted during December 5th to 18th, 2017

Organized by Department of Electronics & Telecommunication Engineering,

Smt. Kashibai Navale College of Engineering, Pune - 411041

Dr. Mrs. S.K. Jagtap

Coordinator and Head, E&TC Dept

Dr. K.R. Borole

Vice Principal

Dr. A.V. Deshpande

Principal



Sinhgad Technical Education Society's
SINHGAD ACADEMY OF ENGINEERING

TM

(Affiliated to University of Pune and Approved by, AICTE, New Delhi.)
S. No. 40/4 A. Near octroi Post, Kondhwa - Saswad Road, Pune - 411048.
☎ : (020) 26934441, 26934550, Fax : 020- 26934297,

E-mail : saeprincipal@sinhgad .edu, Website : www.sinhgad.edu

Department of Electronics and Telecommunication

AICTE Approved 15 days Workshop (05-18 Dec 2017)

ON
Ethics and Moral values

I have attended 15 days AICTE approved workshop on "Ethics and Moral values"
organized by SKN Collage of Engineering Pune.

The key points of the workshop are

1. Meaning of ethics and moral values.
2. Importance of Ethics and Moral values in real life.
3. Understanding the meaning of stress in real life.
4. Stress management.
5. Healthy life style
6. Bhasrika Pranayam
7. Three stage Pranayam
8. Long Sudarshan kriya.
9. Short sudarshan kriya
10. Different Activities of stress management and concentration of brain.


Prof. V. M. Sardeshmukh



Sinhgad Institutes

STES'S

Smt. Kashibai Navale College of Engineering

S.No. 44/1, Vadgaon (BK), Off Sinhgad Road
Pune - 411 041, Maharashtra, India



Celebrating 25th Years

Department of Electronics & Telecommunication Engineering

Certificate

This is to certify that

Mr. / Ms. Vishal D. Bharate

of Institute / Organization SAE, Kondhawa, Pune

Participated in Two Week AICTE sponsored Faculty Development Program

On

"Ethics and Moral Values"

Conducted during December 5th to 18th, 2017

by Department of Electronics & Telecommunication Engineering,
Smt. Kashibai Navale College of Engineering, Pune - 411041

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Department of Electronics and Telecommunication

**AICTE Sponsored Two Week FDP
(05-Dec-2017 TO 18-Dec-2017)**

ON

**“Ethics & Moral Values” At
Smt. Kashibai Navale College of Engineering, Pune -411041**

Summary

The Two Week FDP is arranged by the Smt. Kashibai Navale College of Engineering, Pune, Sponsored by All India Council for Technical Education (AICTE), New Delhi.

The FDP was aimed to develop, understand the basics concept of Ethics & Moral Values. The said workshop was conducted by experts from Shri Shri Ashram. It is for inculcating the moral and ethics in technical education.

They elaborated the Sudarshan Kriya which is really helpful for today's lifestyle. The short Sudarshan kriya as well as long Sudarshan kriya is elaborated properly.

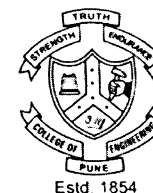
Two week FDP was organized daily in four sessions out of four sessions two sessions based on theory and two based on Kriyas and activities.

Prof. V. D. Bharate



COLLEGE OF ENGINEERING PUNE

(An Autonomous Institute of Govt. of Maharashtra)



AICTE sponsored Two Weeks Faculty Development Program

on

Embedded System Design: A Microcontroller Approach

CERTIFICATE

This is to certify that Minakshee M. Patil
has attended AICTE sponsored two weeks Faculty Development Program on "**Embedded System Design: A Microcontroller Approach**" during November 20, 2017 - December 1, 2017 organized by Department of Instrumentation & Control under the aegis of All India Council for Technical Education (AICTE).

(Ref.: F.No. 6-43/RIFD/FDP/Policy-1/2016-17 dated June 6, 2017)

Dr. Uttam Chaskar

Coordinator

Department of Instrumentation & Control

Prof. C. Y. Patil

Head

Department of Instrumentation & Control

Prof. B. B. Ahuja

Director

College of Engineering Pune





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Department of Electronics and Telecommunication

**AICTE Sponsored Two Week FDP
ON
Embedded System Design – A Microcontroller Approach”**

(November 20, 2017 to December 1, 2017)

Department of Instrumentation and Control, College of Engineering Pune has organized this AICTE Sponsored two weeks Faculty Development Program on “Embedded System Design – A Microcontroller Approach” during November 20, 2017 to December 1, 2017.

An Embedded System can be defined as a Software System that is completely encapsulated by the hardware it controls. Embedded Systems typically have tight constraints on both functionality and implementation. In particular, they must guarantee real time operations reactive to external events, conform to size and weight limits, budget, power and cooling consumption, satisfy safety and reliability requirements, and meet tight cost targets. System is a way of working, organizing or performing one or many tasks according to a fixed set of rules, program or plan. In other words, an arrangement in which all units assemble and work together according to a program or plan. An embedded system is a system that has software embedded into hardware, which makes a system dedicated for an application (s) or specific part of an application or product or part of a larger system. It processes a fixed set of preprogrammed instructions to control electromechanical equipment which may be part of an even larger system. The program will have tutorial flavor and practical hands on experience will be delivered to the participants who are not necessarily Embedded System and related expert. It will also provide a timely update on the most recent developments in this area. This faculty development program provides understanding of Embedded System & embedded system design metrics & architecture,

perspectives of designing embedded system, system design, mixed signal circuit simulation, various microcontrollers, Controller system implementation and debugging, Microcontrollers IDEs, Application specific case studies such as Biomedical Instrumentation, Control and Automation, Mechatronics, etc.

Course Content

Introduction of Embedded Systems, design cycle methodologies, Embedded C and coding standard for Embedded system ,Electronic Circuit Simulation and verification, PIC series architecture, Integrated Development Environment: MPLAB, Hands-on with MPLAB, peripheral interfacing such as I/Os, timer, counters, serial buses, PWM and motors control, etc., case studies TI MSP430 - an ultra-low-power 16-bit microcontroller family from Texas Instruments and IDE: Code Composer Studios Architecture of 16 Bit Microcontrollers, I/O Ports and interfacing, Analog to Digital Conversion, Pulse Width Modulation Using Timers and Interrupts, LED, LDR interfacing, LCD Interfacing, Touch Screen interfacing, Serial Communication, Interrupt programming & Case studies Tiva C Series Controller – Architecture of Tiva Series Controller, I/O Ports and interfacing, Analog to Digital Conversion, Pulse Width Modulation, Using Timers and Interrupts, RTC, LED, LDR interfacing, LCD Interfacing, Touch Screen interfacing, Serial Communication, Interrupt programming Case studies - Sensor interfacing, DC motor, Servos Projects -Measurement and Control, Automation, Medical, Robotics, etc.



Prof Minakshee M Patil



**Dr. Babasaheb Ambedkar Technological
University**

(Established by Government of Maharashtra and Governed by
Dr. Babasaheb Ambedkar Technological University Act No. XXIX of 2014)



Certificate

*This is to certify that Chandrashekhar G. Patil
of Sinhgad Academy of Engg., Kondhawa, Pune
has attended and successfully completed a Two Week
Faculty Development Program on*

***Recent Trends in Signal Representation:
A Multirate Approach***

***sponsored by All India Council for Technical Education
(AICTE), New Delhi (Vide Ref: F. No. 6 - 44/RIFD/FDP/
Policy-1/ 2016-17 dated 6th June, 2017) during November
06-17, 2017 organized by Department of Electronics and
Telecommunication Engineering, Dr. Babasaheb
Ambedkar Technological University.***


Dr. S. L. Nalhawar
Vice-Chancellor



Sinhgad Technical Education Society's
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Department of Electronics and Telecommunication

**AICTE Sponsored Two Week FDP
(06-Nov-2017 TO 17-Nov-2017)**

ON

**“Recent Trends In Signal Representation: A Multirate Approach” At
Dr. Babasaheb Ambedkar Technological University Lonere Dist. Raigad**

Summary

The Two Week FDP is arranged by the Dr. Babasaheb Ambedkar Technological University Lonere, Sponsored by All India Council for Technical Education (AICTE), New Delhi (Vide Ref: F.No.6-44/RFID/FDP/Policy-1/2016-17 Dated 6th June 2017,)

The FDP was aimed to develop, understand the basics concept of Recent Trends In Signal Representation: A Multirate Approach and hands on that are required in our research and teaching learning Process.

Various well known Resource persons from the Industries and different Universities were invited for Presenting their Knowledge and research ideas. The Inaugural function preceded by the Vice Chancellor Dr. Vals Gaikekar DBATU Lonere. He elaborated the global scenario of the education and stressed on the up-gradation of the education policies at present we have.

Two week FDP was organized daily in four sessions out of four sessions two sessions based on theory and two based on hands-on. Imminent personalities from well-known Organizations such as IIT Pawai, SPP University, SGGS Nanded and well known industries, have enlighten the participants from various colleges of India on various issues related to Multirate approach of signal processing research methodology and its application in Education field as well as in Industry.

One day they have planned industrial visit in afternoon session to Reliance Jio Mumbai for getting practical live knowledge.

On last day the concluding ceremony was organized Dr. S.L. Nalbalwar Convener of the program DBATU Lonere was very impressive. The feedbacks from all the participants were collected. The FDP was found to be very informative and use full for all the participants.


Dr. C.G. Patil



JSPM's


Rajarshi Shahu College of Engineering,
Tathawade, Pune - 411033.




AICTE Sponsored Two Weeks' FDP on
"Recent Trends In Internet of Things"

Certificate of Participation

This is to certify that Mr./Ms. Rajesh Uttamrao Yawale of
Sinhgad Academy of Engg, Kondhwa has attended AICTE Sponsored Two weeks
FDP on "Recent Trends in Internet of Things" organized by JSPM's Rajarshi Shahu
College of Engineering, Pune held during 4th December to 15th December 2017.


Dr. A. N. Paithane
(HOD, E&TC)


Dr. Mrs. S. C. Patil
(Convener, Research Coordinator)


Dr. R. K. Jain
(Principal, RSCOE)



Sinhgad Institutes

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E-mail : saeprincipal@sinhgad.edu, Website : www.sinhgad.edu

TM

Department of Electronics and Telecommunication

AICTE Sponsored Two Week FDP
(04-Dec-2017 TO 15-Dec-2017)

ON

“Recent Trends In Internet of Things”

At

Rajarshi Shahu College of Engineering, Tathawade Pune-33

Summary

The Two Week FDP is arranged by the Rajarshi Shahu College of Engineering Tathawade Pune in association with AICTE New Delhi. The FDP was aimed to develop, understand the basics concept and hands on Internet of Things that are required in our research and teaching learning Process.

Various well known Resource persons from the Industries and different Universities were invited for Presenting their Knowledge and research ideas. The Inaugural function preceded by the Principal Dr R K Jain of RSCOE Pune. He elaborated the global scenario of the education and stressed on the up-gradation of the education policies at present we have.

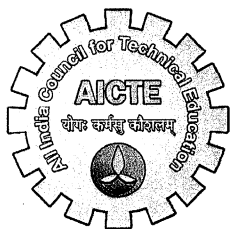
Two week FDP was organized daily in four sessions out of four sessions two sessions based on theory and two based on hands-on. Imminent personalities from well known Organizations such as IIT Pawai, SPP University and well known industries, have enlighten the participants from various colleges of India on various issues related to Internet of things Research methodology and its application in Education field as well as in Industry.

One day they have planned industrial visit in afternoon session to TCS at Hinjewadi, Pune for getting practical knowledge.

On last day the concluding ceremony was organized Dr. Mrs S C Patil Convener of the program RSCOE Pune and Speech of Dr. R K Jain principal RSCOE Pune was very impressive. The feedbacks from all the participants were collected.

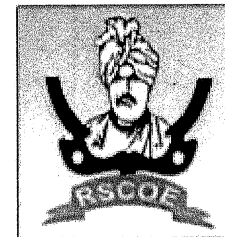
The FDP was found to be very informative and use full for the participants.

Prof. R.U. Yawle



JSPM's

**Rajarshi Shahu College of Engineering,
Tathawade, Pune - 411033.**





**AICTE Sponsored Two Weeks' FDP on
"Recent Trends In Internet of Things"**

Certificate of Participation

This is to certify that Mr./Ms. Atul B. Ingole of Sinhgad Academy of Engg., Kondhwa has attended AICTE Sponsored Two weeks FDP on "Recent Trends in Internet of Things" organized by JSPM's Rajarshi Shahu College of Engineering, Pune held during 4th December to 15th December 2017.


Dr. A. N. Paithane
(HOD, E&TC)


Dr. Mrs. S. C. Patil
(Convener, Research Coordinator)


Dr. R. K. Jain
(Principal, RSCOE)



Sinhgad Institutes

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Department of Electronics and Telecommunication

AICTE Sponsored Two Week FDP

(04-Dec-2017 TO 15-Dec-2017)

ON

“Recent Trends In Internet of Things”

At

Rajarshi Shahu College of Engineering, Tathawade Pune-33

Summary

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The FDP was found to be very informative and use full for the participants.


Prof. A.B. Ingole



**Dr. Babasaheb Ambedkar Technological
University**

(Established by Government of Maharashtra and Governed by
Dr. Babasaheb Ambedkar Technological University Act No. XXIX of 2014)



Certificate

*This is to certify that **Atul B. Ingole**
of **Sinhgad Academy of Engg., Kondhawa, Pune**
has attended and successfully completed a Two Week
Faculty Development Program on*

***Recent Trends in Signal Representation:
A Multirate Approach***

*sponsored by All India Council for Technical Education
(AICTE), New Delhi (Vide Ref: F. No. 6 - 44/RIFD/FDP/
Policy-1/ 2016-17 dated 6th June, 2017) during November
06-17, 2017 organized by Department of Electronics and
Telecommunication Engineering, Dr. Babasaheb
Ambedkar Technological University.*

Dr. S. L. Nalbalwar
FDP Coordinator &
Head, Dept. of E & TC



Sinhgad Institutes

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Department of Electronics and Telecommunication

**AICTE Sponsored Two Week FDP
(06-Nov-2017 TO 17-Nov-2017)**

ON

**“Recent Trends in Signal Representation: A Multirate Approach”
at Dr. Babasaheb Ambedkar Technological University Lonere Dist. Raigad**

Summary

The Two Week FDP is arranged by the Dr. Babasaheb Ambedkar Technological University Lonere, Sponsored by All India Council for Technical Education (AICTE), New Delhi (Vide Ref: F.No.6-44/RFD/FDP/Policy-1/2016-17 Dated 6th June 2017)

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Prof. A.B. Ingole



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University**

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Certificate

*This is to certify that Vishal D. Bharate
of Sinhgad Academy of Engg., Kondhawa, Pune
has attended and successfully completed a Two Week
Faculty Development Program on*

*Recent Trends in Signal Representation:
A Multirate Approach*

*sponsored by All India Council for Technical Education
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Telecommunication Engineering, Dr. Babasaheb
Ambedkar Technological University.*


Dr. S. L. Nalbalwar
FDP Coordinator &
Head, Dept. of E & TC

Vidyavihar, Lonere
Tal: Mangaon, Dist: Raigad (MS) - 402103



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Department of Electronics and Telecommunication

AICTE Sponsored Two Week FDP
(06-Nov-2017 TO 17-Nov-2017)
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Prof. V. D. Bharate




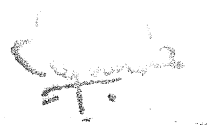
Sinhgad Technical Education Society's
Smt. Kashibai Navale College of Engineering, Pune-41.
Department of Engineering Sciences
(2017 – 18)

Faculty Orientation Program
TRAIN THE TRAINER
Department of Engineering Sciences

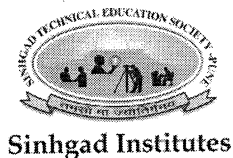
Mr/Ms D S Dabi of SAE has attended The Train The Trainer Program of FE for the subject Basic Electronics Engineering conducted in Department of Engineering Sciences, Smt. Kashibai Navale College of Engineering Vadgaon(Bk), Pune during 19-6-2017 to 24-6-2017.


Prof. R. B. Jadhav
FOP CO-ORDINATOR


Prof. M. S. Alandkar
HOD


Dr. A. V. Deshpande
PRINCIPAL





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Department of Electronics and Telecommunication

Train The Trainer (19th - 24th June 2017) **on** **Basic Electronics Engineering**

This was a One week FOP held at SKNCOE pune on 19th & 24th June 2017 organized by Department of Engineering Sciences for making teachers more aware of how to teach Basic Electronics Engineering and to give some hints & techniques so that teacher student relation will be more friendly and effective two way communication will take place.

Main Moto of workshop was making teachers awareness and to share resources and study material of Basic Electronics Engineering among all the faculties in STES.

Mr. Dhiraj Sanjay Dabi



Sinhgad Technical Education Society's

Smt. Kashibai Navale College of Engineering, Pune-41.
Department of Engineering Sciences
(2017 – 18)

Faculty Orientation Program

TRAIN THE TRAINER

Department of Engineering Sciences

Mr/Ms S S Shah of SAE has attended The Train The Trainer Program of TE for the subject Basic Electronics Engineering conducted in Department of Engineering Sciences, Smt. Kashibai Navale College of Engineering Vadgaon(BK), Pune during 19-6-2017 to 24-6-2017.

Prof. R. B. Jadhav
FOP CO-ORDINATOR

Prof. M. S. Alandkar
HOD

Dr. A. V. Deshpande
PRINCIPAL





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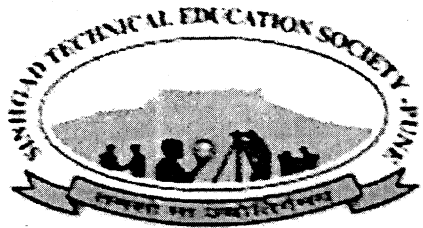
Department of Electronics and Telecommunication

Train The Trainer (19th - 24th June 2017)
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Suyog Sanjay Shah

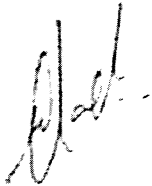



Sinhgad Institutes


TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms R. V. Yawle of SAOE
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electrical Circuits & Machines, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Bokhande
Principal, SCOE



Sinhgad Institutes

Sinhgad Technical Education Society's

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Department of Electronics and Telecommunication

Summary of Train The Trainers Program-SE E&TC/Elex 2015 Curriculum

The above said workshop was organized at Sinhgad college of Engineering Vadgaon ,Pune from 5th to 10th June 2017. The objective of this workshop was to orient all the faculty towards the revised syllabus of SE(E&TC/Elex) and to bring in uniformity in teaching across all the colleges under the STES of Pune.

On the first day session started with Inauguration Function. The First session was based on first unit. Kirchoff's Current and Voltage Laws, Independent and dependent sources, Mesh, Super mesh, Node and Super Node analysis. Source transformation and source shifting concepts were discussed. Along with this numericals on various theorems such as Superposition, Thevenin's, Norton's and Maximum Power Transfer Theorems, Millers Theorem and its dual were solved.

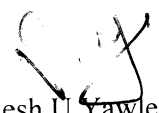
Second day Session was on the second unit i.e. Transformer. Construction, Transformer on No-load (Transformation ratio, emf equation), impedance transformation, losses in transformer, were discussed that need to be explained to students.

Third day we discussed about on 3rd Unit which consist of DC Machine and AC machine, their construction, working, and characteristics.

On the fourth day, Session started with discussion on fourth unit this way all six units were discussed on six days

On last day the concluding ceremony was organized .Prof. M. M. Jadhav Coordinator of the program. The feedbacks from all the participants were collected.

The TTT Program was found to be very informative and use full for the Engineering faculties.


Mr Rajesh U Yawle
Assistant Professor,
E&Tc Dept,
SAOE, Kondhwa



Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms S. V. Thakur of SAE, Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Data Structures & Algorithms, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mall
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

Department of Electronics and Telecommunication

Summary of Train The Trainer workshop on SE E&TC 2017 Course For Subject Data Structures And Algorithm

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadagaon, Pune from **5th to 10th June 2017**. During Workshop revised Second year syllabus and contents were discussed for Data Structure and Algorithm. One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. R. P. Borse from SAE, Kondhwa, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by Prof. S. K. Sahastrabuddhe from SKNCOE, Pune on the second unit. Second Unit is based on Searching and sorting techniques. During this session Prof. S. K. Sahastrabuddhe explained the analysis of algorithm with reference to time and space complexity for searching & sorting techniques that need to be explained to students.

Third day we discussed about on 3rd Unit which consist of Stack and Queue. The unit was discussed by Prof. S. B. Takale from SCOE, who not only explained the concept, method but also explained the application based on the unit which need to be discussed with students.

On the fourth day, Session started with discussion on 4th Unit which is based on Linked List. The speaker for the session was Prof. N. N. Pachpore, who not only explained the concept, method but also explained the application based on the unit which need to be discussed with students.

Fifth day we discussed about on 5th Unit which consist of Non Linear Data type viz. Tree. The unit was discussed by Prof. D. D. Mondal from NBN, Narhe, who not only explained the concept, method but also explained the application based on the unit which need to be discussed with students.

Sixth day we discussed about on 6th Unit which consist of Non Linear Data type viz. Graphs. The unit was discussed by Prof. N. P. Bhojne from SCOE, who not only explained the concept, but the Speaker for the session brought to our notice that the unit is important with respect to End Semester theory exam since maximum weightage is on these units. Different algorithm such as Minimum Spanning tree, Prim's Algorithm etc were discussed during this session and ways to explain to the students so as to solve the problem on them.

Workshop ended where participation certificates were given to all the participants.



S V Thakur
Asst Prof.
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER
T.E. E&TC
Certificate

Mr/Ms C. P. Mahajan of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Micro controllers, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Summary

of

Train The Trainer workshop on TE E&TC 2017 Course For Subject Microcontrollers.

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadagaon, Pune from **5th to 10th June 2017**. During Workshop revised Third year syllabus and contents were discussed for **Microcontrollers**.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. **Dr. Dnyaneshwar S Mantri** from Sinhgad institution of Technology, Lonavala, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by **Prof. H. N.Lokhande** from NBN Sinhgad school of Pune on the second unit. Second Unit is based on Interfacing of LEDS, LCD, Keypad, 7 segment Multiplexed Display, ADC 0809, Programming Environment: Study of software development tool chain (IDE), hardware debugging tools (timing analysis using logic analyser) and different programs based on 8051.

Third day we discussed about on 3rd Unit which consist of Parallel Port Interfacing-II. The unit was discussed by **Prof. Kirti Yadav** from SKNSITS, Lonavala, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students.

On the fourth day, Session started with discussion on 4th Unit which is based PIC Microcontroller Architecture. The speaker for the session was **Prof. R. R. Deshmukh** from SCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

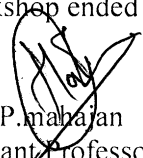
Fifth day we discussed about on 5th Unit which consist of Real World Interfacing Part I (Based on PIC Microcontroller). The unit was discussed by **Prof. S. A. Panwar** from SKNCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Sixth day we discussed about on 6th Unit which consist of Real World Interfacing Part II

Basics of Serial Communication Protocol: Study of RS232, RS 485, I2C, SPI, MSSP structure (SPI & I2C), UART, Sensor interfacing using ADC, RTC (DS1306) with I2C and EEPROM with SPI. Design of PIC test Board, Home protection System: All programs in embedded C.

The unit was discussed by **Prof. J. A. Bagban** from SAE, Kondhwa, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Workshop ended where participation certificates were given to all the participants.


Mr. C.P. Mahajan
Assistant Professor
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER
S.E. E&TC
Certificate

Mr/Ms S. M. Parvat of SAE, Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Data Structures & Algorithms, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Summary of

Train The Trainer workshop on SE E&TC 2017 Course For Subject Data Structures And Algorithm

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadagaon, Pune from **5th to 10th June 2017**. During Workshop revised Second year syllabus and contents were discussed for Data Structure and Algorithm.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. R. P. Borse from SAE, Kondhwa, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by Prof. S. K. Sahastrabuddhe from SKNCOE, Pune on the second unit. Second Unit is based on Searching and sorting techniques. During this session Prof. S. K. Sahastrabuddhe explained the analysis of algorithm with reference to time and space complexity for searching & sorting techniques that need to be explained to students.


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On the fourth day, Session started with discussion on 4th Unit which is based on Linked List. The speaker for the session was Prof. N. N. Pachpore, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

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Workshop ended where participation certificates were given to all the participants.


Ms. S. M. Parvat
Assistant Professor
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER

S.E. E&TC

Certificate

Mr/Ms R. D. Borse of SAE, Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Data Structures & Algorithms, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

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Summary

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Train The Trainer workshop on SE E&TC 2017 Course For Subject Data Structures And Algorithm

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Workshop ended where participation certificates were given to all the participants.


Dr R P Borse, Asso Prof.
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER
S.E. E&TC
Certificate

Mr/Ms Vishal D. Bharate of SAE Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Signals & systems, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

Department of Electronics and Telecommunication

**Summary
of
Train The Trainers Program-SE E&TC/Elex 2015 Curriculam**

The above said workshop was organized at Sinhgad college of Engineering Vadgaon ,Pune from 5th to 9th June 2017. The objective of this workshop was to orient all the faculty towards the revised syllabus of SE(E&TC/Elex) and to bring in uniformity in teaching across all the colleges under the STES of Pune.

On the first day session started with Inauguration Function. The First session was based on Unit-1 Introduction to signals and systems which is based on basics of this subject. We discussed multiple choice questions as well as theoretical questions..

Second day Session was on the second unit, Time domain analysis of LTI system, where we discuss about integration and summation.

Third day we discussed about third unit which consist of Fourier series in continuous and discrete time .

On the fourth day, Session started with discussion on fourth unit Fourier Transform where we discuss properties and application of fourier transform.

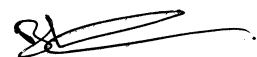
On the fifth day, Session started with discussion on fifth unit which is Laplace transform. We discussed Inverse laplace transform, Application stability condition using laplace transform.

On the sixth/last day, Session started with discussion on sixth unit which is Probability and random signals .Types of Probability and random variables are discussed.

This way all six units were discussed in six days

On last day the concluding ceremony was organized .Prof. M. M. Jadhav Coordinator of the program. The feedbacks from all the participants were collected.

The TTT Program was found to be very informative and use full for the Engineering faculties.



Mr. V. D. Bharate
Assistant Professor,
E&Tc Dept,
SAOE, Kondhwa





Sinhgad Institutes


TRAIN THE TRAINER
S.E. E&TC

Certificate

Mr/Ms Ruchi . A . Jain of SAE, Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Signals & systems _____, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE



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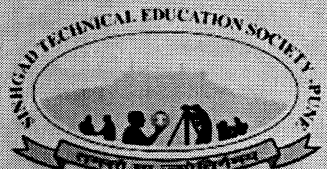
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The TTT Program was found to be very informative and use full for the Engineering faculties.


Mrs. R.A. Jain
Assistant Professor,
E&Tc Dept,
SAOE, Kondhwa

Subject:- Signals and Systems



Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms R. S. V. Patil of SAOE
has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electrical Circuits & Machines, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Summary

of

Train The Trainer workshop on SE E&TC 2017 Course For Electrical Circuits and Machine

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadgaon, Pune from **5th to 10th June 2017**. During Workshop revised Second year syllabus and contents were discussed for **Electrical Circuits and Machine**.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. S. A. Phalke from SIT, Lonavla, highlighted the first unit syllabus and gave understanding about solving circuits using various laws and theorems..

Second day was conducted by Prof. R. U. Yawle from SAE, Kondwa, Pune on the second unit. Second Unit is based on Transformers. It's working operation and types were discussed thoroughly.

Third day we discussed about on 3rd Unit which consist of DC Machine. The unit was discussed by Prof. V. G. Rajeshwarkar from SKN, Vadgaon, who not only explained the constructions, working but also had taken numerical based on it.

On the fourth day, Session started with discussion on 4th Unit which is based on AC machine. The speaker for the session was Prof. Roma Goregaonkar from RMD, Warje, discussed the working construction along with characteristics and numerical.

Fifth day we discussed about on 5th Unit Special Motors -I. The unit was discussed by Prof. Yadav Kirti from SCOE, Vadgaon. In this unit we had discussed regarding BLDC motor, sensors and Servomotors. We also had discussed BLDC motor practically.

Sixth day we discussed about on 6th Unit which consist of . Special Motors -II. The unit was discussed by Prof. K. R Dubai from SCOE, Vadgaon. In this unit we had discussed regarding Stepper motor and Induction motor along with their characteristics.

Workshop ended where participation certificates were given to all the participants.


Dr. Mrs. Rasmi V. Patil
Assistant Professor
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms Sachin B. Pawar of SAE, Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electronic Devices & Circuits _____, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
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Department of Electronics and Telecommunication

Summary of Train The Trainers Program-SE E&TC/Elex 2015 Curriculam

Train the Trainer Program was organized at Sinhgad college of Engineering Vadgaon, Pune, from 5th to 10th June 2017. The objective of this workshop was to orient all the faculty towards the revised syllabus of S.E(E&TC) and to bring in uniformity in teaching across all the colleges under the STES of Pune.

On the first day session started with Inauguration Function. The First session was based on Unit-1 Junction Field Effect transistor which is based on basics of JFET, DC & AC analysis. With this we discussed multiple choice questions as well as theoretical questions.

Second day Session was on the second unit, MOSFET and it's DC analysis, where we discuss about Construction, Working and Operation and DC analysis of MOSFET.

Third day we discussed about third unit which MOSFET AC analysis with Numerical.

On the fourth day, Session started with discussion on fourth unit MOSFET circuits where we discuss various MOSFET circuits.

On the fifth day, Session started with discussion on fifth unit which is Feedback Amplifier and Oscillators. We discussed Four types of amplifiers, Feedback topologies and effect of feedback on amplifiers.

On the sixth, last day Session started with discussion on sixth unit which is Voltage Regulator we discussed block diagram of an adjustable three terminal positive and negative regulators (317,337). Typical connection diagram, current boosting, SMPS.

This way all six units were discussed in Train the Trainer Program for six days.

On last day the concluding ceremony was organized. Prof. M. M. Jadhav was coordinator of the program. The feedbacks forms, from all the participants were collected. Thus Train the Trainer Program was found to be very informative and use full for the Engineering faculties.

Mr. S.B. Pawar
Assistant Professor,
E&Tc Dept,
SAOE, Kondhwa



Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms Atul B. Ingole of SAE, Kondhwa
has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electronic Devices & Circuits, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
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Second day Session was on the second unit, MOSFET and it's DC analysis, where we discuss about Construction, Working and Operation and DC analysis of MOSFET.

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Mr. A.B. Ingole
Assistant Professor,
E&Tc Dept,
SAOE, Kondhwa





Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC *Certificate*

Mr/Ms C. C. Athavale of SAE
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Digital Electronics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
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Dr. S. D. Lokhande
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Sinhgad Technical Education Society's
SINHGAD ACADEMY OF ENGINEERING

(Affiliated to University of Pune and Approved by, AICTE, New Delhi.)
S. No. 40/4 A. Near octroi Post, Kondhwa - Saswad Road, Pune - 411048.
☎ : (020) 26934441, 26934550, Fax : 020- 26934297,
E-mail : saeprincipal@sinhgad .edu, Website : www.sinhgad.edu

Department of Electronics and Telecommunication

**Summary
of
Train The Trainer workshop on SE E&TC 2017 Course
For Digital Electronics**

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadgaon, Pune from **5th to 10th June 2017**. During Workshop revised Second year syllabus and contents were discussed for **Digital Electronics**

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. P. S. Deshpande from SKN, highlighted the first unit syllabus and Explained the design procedure of combinational Logic Design..

Second day was conducted by Prof .R.B.Dhumale form SCOE, Vadgaon, Pune on the second unit. Second Unit is based on Sequential Logic Design.

Third day we discussed about on 3rd Unit which consist of State Machine. The unit was discussed by Prof. C.C.Athavale from SAE, Kondhwa,Pune.

On the fourth day, Session started with discussion on 4th Unit which is based on Digital Logic Families. The speaker for the session was Prof.A.B.Kanwade discussed the different types of IC families and comparative Studies.

Fifth day we discussed about on 5th Unit PLD and Semiconductor memories. The unit was discussed by Prof. S.S.Begampure. In this unit we had discussed regarding types of PLDs and Semiconductor memories and Design using PLDs.

Sixth day we discussed about on 6th Unit which is Introduction to 8051. The unit was discussed by Prof. V. H.Yadav from SCOE,Vadgaon. In this unit we had discussed regarding architecture of 8051,instruction Set and programming.

Workshop ended where participation certificates were given to all the participants.

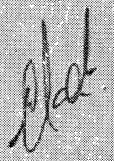

Mrs.C.C.Athavale
Assistant Professor
SAE,E&TC





Sinhgad Institutes

TRAIN THE TRAINER
S.E. E&TC
Certificate

Mr/Ms S.V. Salunkhe of SADE
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electrical Circuits & Machines, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE

Prof. Shruti V. Salunkhe

A Report On Train The Trainer S.E. E & TC (TTT)

Subject: Electrical Circuits And Machines

DATE: 5th June to 10th June 2017

Day 1 : Unit 1 : Basic Circuit Analysis and Simplification Techniques

Session1: Teaching of unit 1, Topic: Introduction

Session2: Topic : Network Analysis & Network Theorems.

Session3: Discussions on unit 1.

Day 2: Unit 2 : Transformer

Session 1: Topics : Types , construction , Transformer on no load

Session 2: Topics : Impedance , losses in transformer , C.T. , P.T. High frequency transformer

Session 3: Discussions on unit 2.

Day 3: Unit 3: DC Machines

Session 1: Construction of DC Machine, Motoring and generation action, types, EMF equation, Torque equation (Torque-armature current characteristics, Torque-speed characteristics,

Session 2 :

Different methods of speed control, different types of starters for DC shunt motor.
Permanent Magnet DC motors, Applications of DC Motors

Session 3: Discussions on unit 3.

Day 4: AC Motors

Session 1 :

Three phase Induction motors, construction and principle of operation, types, slip and torque equation, Torque-slip characteristics, condition for maximum torque

Session 2 :

Synchronous motors: Construction, principle of operation, characteristics (V curves) and applications

Session 3: Discussions on unit 4.

Day 5: Special Motors 1

Session 1 :

BLDC Motor, Construction, principle, characteristics, control circuit, sensors,

Session 2 :

Construction, principle & applications of Reluctance Motor, Universal Motor.

Session 3: Discussions on unit 5.

Day 6: Special Motors 2

Session 1 :

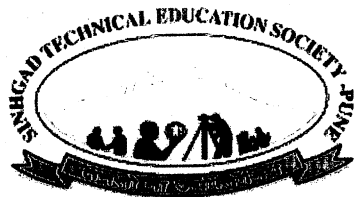
Construction, types, principle, Characteristics, control circuit & applications of Stepper Motors.

Session 2 :

Construction, principle, characteristics, Types and applications of single phase Induction Motor.

Session 3: Discussions on unit 6.





Sinhgad Institutes

TRAIN THE TRAINER
S.E. E&TC
Certificate

Mr/Ms S. A. Banekar of SAE
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Digital Electronics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

One Week Workshop (05-10 June 2017) **on** **Faculty Orientation**

This was a faculty orientation workshop held at Sinhgad College of Engineering (SCOE) Pune from 05-10 June 2017 organized by SCOE for all faculties of SE E&TC from STES for the subject Digital Electronics (DE). One day for each unit was given to discuss its theoretical as well as practical aspects.

Each unit was discussed in detail with numerical asked for university exam. Along with it MCQ's were also discussed and shared with all faculties. As first 3 units are almost based on numerical, previously university asked numerical were discussed and shared. Some concepts with multiple ways to elaborate same were discussed and easiest way found out.

We shared course material with each other like assignments, notes, MCQ questions, teaching plan and Question bank etc which will be definitely beneficial to all.

We discussed about gap in syllabus and tried to define content beyond syllabus to bridge it up. Innovative teaching methods also discussed and various options were find out to fulfill it.



Mr. S. A. Bankar

SAE Kondhwa



Sinhgad Institutes

TRAIN THE TRAINER S.E. E&TC

Certificate

Mr/Ms Sandip M Kasar of SAOE Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Signals & systems, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Summary of Train The Trainer workshop on SE E&TC 2017 Course For Subject Signal and systems

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadagaon, Pune from 5th to 10th June 2017. During Workshop revised Second year syllabus and contents were discussed for Signal and systems.
One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. Ganesh Gaikwad from SCOE Vadgaon, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

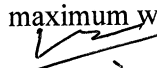
Second day was conducted by Prof. Pujari from SKNCOE, Pune on the second unit "Time Domain Representation of LTI System.

Third day we discussed about on 3rd Unit which consist of Stack and Queue. The unit was discussed by Prof. R.A Jain from SAE, Kondhwa who not only explained the concept, method but also explained the application based on the unit which need to discussed with students.

On the fourth day, Session started with discussion on 4th Unit which is based on Fourier Transform. Prof. Vikas Khetmali, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Fifth day we discussed about on 5th Unit which consist of Laplace Transform The unit was discussed by Prof. Navale from SKNCOE who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Sixth day we discussed about on 6th Unit which consist of Probability by Prof. Dike from SIT Lonavala, who not only explained the concept, but the Speaker for the session brought to our notice that the unit is important with respect to End Semester theory exam since maximum weightage is on these units.


S.M Kasar
Asst Prof.
SAE,E&TC



Sinhgad Institutes

TRAIN THE TRAINER
S.E. E&TC
Certificate

Mr/Ms Potdar Sarika M. of SAE Kondhwa
_____ has attended Train The Trainer Program on S.E. (E&TC) for the
subject Electronics Devices & Circuits, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

A Report on train the teacher One Week workshop

Subject: Electronic Devices and Circuits

DATE: 5th June to 10th June 2017

Day 1 : UNIT 1: JFET Circuits By Prof. A.S.Shirsat

Introduction to JFET, Types, Construction, Operation, Static Characteristics, Pinch off voltage, FET Volt-Ampere characteristics, FET Configurations (CS/CD/CG) and their Comparison. Biasing of FET (Self). FET as an amplifier and its analysis (CS) and its frequency response. Small signal model, FET as High Impedance circuits

Day 2: UNIT 2: MOSFET DC Circuits By Prof. A.B.Ingole

Basics of MOS Transistor operation, Construction of n-channel E-MOSFET, E-MOSFET characteristics & parameters, non-ideal voltage current characteristics viz. Finite output resistance, body effect, sub-threshold conduction, breakdown effects and temperature effects. Common source circuit, Load Line & Modes of operation, common MOSFET configurations: DC Analysis, constant current source biasing..

Day 3: UNIT 3: MOSFET AC Circuit Analysis By Prof. M.M.Wankhade

The MOSFET CS small signal amplifier, Small signal parameters, small signal equivalent circuit, Modeling, Body effect, Analysis of CS amplifier. Introduction to BiCMOS technology. The MOSFET internal capacitances and high frequency model. Introduction to MOSFET as basic element in VLSI, V-I characteristic equation in terms of W/L ratio, MOSFET scaling and small geometry effects, MOSFET capacitances..

Day 4 : UNIT 4: MOSFET Circuits By Prof.H.B.Mali

MOSFET as switch, diode/active resistor, Current sink and source, current mirror, Voltage references, Basic principle of band gap reference, CMOS Inverter as amplifier: Active load, Current source and Push pull configurations.

Day 5: UNIT 5: Feedback amplifiers and Oscillators By Prof. K.J.Amrutkar

Four types of amplifiers. Feedback topologies. Effect of feedback on terminal characteristics of amplifiers. Examples of voltage series and Current series FET feedback amplifiers and their analysis. Barkhausen criterion, stability with feedback. General form of LC oscillator. FET RC Phase Shift oscillator, Wein bridge oscillator, Hartley and Colpitts oscillators.

Day 6 : UNIT 2: Voltage Regulator By Prof. S.Y.Tamboli

Block diagram of an adjustable three terminal positive and negative regulators (317,337). Typical connection diagram, current boosting. Low drop out voltage regulators. Introduction to Switch Mode Power supply (SMPS), Block diagram of SMPS, Types of SMPS. Comparison of Linear Power supply and SMPS.


Mrs.Potdar S.M.



Sinhgad Institutes

TRAIN THE TRAINER

T.E. E&TC

Certificate

Mr/Ms B. S. Gayal of SAE, Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital Communication, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

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Coordinator

Dr. M. B. Mali
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Department of Electronics and Telecommunication

Train The Trainer Program **(05th to 10th June 2017)**

Subject: DC Class: TE (E&TC) AY: 2017-18

This was a six day program for training the trainers for revised Digital Communication (DC) syllabus of TE (E&TC) at SCOE, Vadgaon for 6 days between 05 June 2017 to 10 Jun 2017. The participants were all the teachers who would be teaching the Revised Syllabus for DC from 2017 from all STES colleges.

Each day was devoted to teaching one unit of the revised syllabus of DC w.e.f. June 2017. There were six resource persons from different STES colleges such as SCOE, SKNCOE, SAE, SITL, SITS, RMD. Every day was broken into 3 sessions. The first two sessions were devoted to the teaching of the theory contents and tutorials for the particular unit. The third session was devoted to carrying out the laboratory assignments (Practical) associated with the particular unit. The workshop provided valuable insights to teaching methodologies which were useful especially for teachers who were taking the subject for the very first time.



Mr. Gayal B.S.

Asst. Professor,
SAE, Kondhwa




Sinhgad Institutes

TRAIN THE TRAINER T.E. E&TC

Certificate

Mr/Ms Y.R. Bachkar of SAE Konchwa.
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital signal processing, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE

A Report On

Train the trainer for TE (E&TC) revised syllabus 2017.

DIGITAL SIGNAL PROCESSING

A train the trainer on TE (E&TC) Revised Syllabus 2017 Course for the subject Digital Signal Processing was organized by STES, Pune between 05th to 11th June 2017.

Objective:

The objective of this workshop was to orient all the faculty towards the revised syllabus of TE (E&TC) and to bring uniformity in teaching across all the colleges under Sinhgad group.

DAY-1: 05/06/2017.

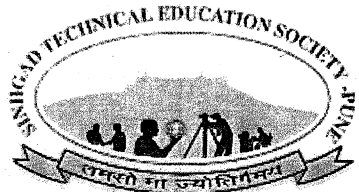
Dr. V. N. Rohakale conducted first two sessions on DSP Preliminaries and applications. She covered topic sampling theorem in time domain, representation of signals as vectors, concept of Basis function and orthogonality, Eigen values, Eigen vector, Basic elements of DSP and its requirements, advantages of Digital over Analog signal processing. In the last session of the day she covered matlab program on sampling for different conditions.

DAY-2: 06/06/2017.

Dr. S. A. Newaskar conducted second day. In first two sessions topics which he covered are DTFT, DFT and its Properties, circular convolution, linear convolution, Computation of linear convolution using circular convolution, FFT, decimation in time and decimation in frequency using Radix-2 FFT algorithm, Linear filtering using overlap add and overlap save method, Introduction to Discrete Cosine Transform. In the last session all the matlab programs based on DFT are discussed.

DAY-3: 06/06/2017.

Third session is taken by Mrs deepali Newaskar. She covered Need for transform, relation between Laplace transform and Z transform, between Fourier transform and Z transform, Properties of ROC and properties of Z transform, Relation between pole location and time domain behavior, causality and stability considerations for LTI systems, Inverse Z transform, Power series method, partial fraction expansion method, Solution of difference equations. Matlab program is discussed in the last session.



Sinhgad Institutes

TRAIN THE TRAINER
T.E. E&TC
Certificate

Mr/Ms V.P. Patil of SAOE, Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Mechatronics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Five days Train the trainer Workshop (05 -10 June 2017)
ON
“Mechatronics T.E. (E&TC) Revised syllabus 2015 Course”
At
SCOE, Pune

Train the trainers workshop on “Mechatronics T.E.(E&TC) Revised Syllabus 2015 course”, under STES's organized in association with E&TC department of SCOE, Pune

As per the usual practice of the STES, the all syllabus along with the all chapters of the subject is discussed, where all teaching staff from various colleges from STES are participated and after discussion different aspects of respective subject the contents of the subjects are revised. In the discussion main focus is on the how to conduct the practical's.

The workshop was divides in six Days . Each day was given for each unit. In the First session inauguration was there, giving the guidilines for the workshop to Dr.S.D. Lokhande.

During the workshop invited expert teachers from different colleges given the guideline for teaching this subject. How to teach a particular unit and what will be teaching method discussed.

Unit I :Introduction to Mechatronics (NBN, Prof. Kulkarni)

Unit II :Overview of Sensors, Transducers (SITS, Prof. Lohar)

Unit III : Hydraulic Systems(SITS Narhe, Prof. Kulkarni)

Unit IV : Pneumatic Systems(SAE, Prof. Vanita Patil)

Unit V : Electron-Mechanical Actuators (SCOE, Prof. M.N.Kakatkar)

Unit VI :Mechatronics Systems in Automobile(SKNCOE, Prof. Sunita Deshmukh)


Prof. V.P. Patil



Sinhgad Institutes

TRAIN THE TRAINER T.E. E&TC

Certificate

Mr/Ms K.N. Jagtap of SAOE, Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Mechatronics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Five days Train the trainer Workshop (05 -10 June 2017)
ON
“Mechatronics T.E. (E&TC) Revised syllabus 2015 Course”
At
SCOE, Pune

Train the trainers workshop on “Mechatronics T.E.(E&TC) Revised Syllabus 2015 course”, under STES's organized in association with E&TC department of SCOE, Pune

As per the usual practice of the STES, the all syllabus along with the all chapters of the subject is discussed, where all teaching staff from various colleges from STES are participated and after discussion different aspects of respective subject the contents of the subjects are revised. In the discussion main focus is on the how to conduct the practical's.

The workshop was divides in six Days . Each day was given for each unit. In the First session inauguration was there, giving the guidilines for the workshop to Dr.S.D. Lokhande.

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Unit I :Introduction to Mechatronics (NBN, Prof. Kulkarni)

Unit II :Overview of Sensors, Transducers (SITS, Prof. Lohar)

Unit III : Hydraulic Systems(SITS Narhe, Prof. Kulkarni)

Unit IV : Pneumatic Systems(SAE, Prof. Vanita Patil)

Unit V : Electron-Mechanical Actuators (SCOE, Prof. M.N.Kakatkar)

Unit VI :Mechatronics Systems in Automobile(SKNCOE, Prof. Sunita Deshmukh)


Prof. K.N.Jagtap



Sinhgad Institutes

TRAIN THE TRAINER
T.E. E&TC
Certificate

Mr/Ms J. A. Bagban of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Micro controllers _____, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



Sinhgad Institutes

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Department of Electronics and Telecommunication

Summary

of

Train The Trainer workshop on TE E&TC 2017 Course For Subject Microcontrollers.

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadgaon, Pune from 5th to 10th June 2017. During Workshop revised Third year syllabus and contents were discussed for **Microcontrollers**.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. **Dr. Dnyaneshwar S Mantri** from Sinhgad institution of Technology, Lonavala, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by **Prof. H. N.Lokhande** from NBN Sinhgad school of Pune on the second unit. Second Unit is based on Interfacing of LEDS, LCD, Keypad, 7 – segment Multiplexed Display, ADC 0809, Programming Environment: Study of software development tool chain (IDE), hardware debugging tools (timing analysis using logic analyser) and different programs based on 8051.

Third day we discussed about on 3rd Unit which consist of Parallel Port Interfacing-II. The unit was discussed by **Prof. Kirti Yadav** from SKNSITS, Lonavala, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students.

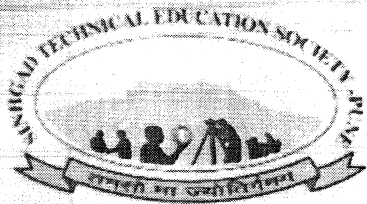
On the fourth day, Session started with discussion on 4th Unit which is based PIC Microcontroller Architecture. The speaker for the session was **Prof. R. R. Deshmukh** from SCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Fifth day we discussed about on 5th Unit which consist of Real World Interfacing Part I (Based on PIC Microcontroller). The unit was discussed by **Prof. S. A. Panwar** from SKNCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Sixth day I was the resource person for 6th Unit which consist of Real World Interfacing Part II Basics of Serial Communication Protocol: Study of RS232, RS 485, I2C, SPI, MSSP structure (SPI & I2C), UART, Sensor interfacing using ADC, RTC (DS1306) with I2C and EEPROM with SPI. Design of PIC test Board, Home protection System: All programs in embedded C.

We had a discussion on applications based on the unit which need to discussed with students

Ms. J. A. Bagban.
Assistant Professor
SAE, E&TC



Sinhgad Institutes

TRAIN THE TRAINER T.E. E&TC *Certificate*

Mr/Ms R.B. Kakkeri of SAOE
has attended Train The Trainer Program on T.E. (E&TC) for the
subject Electromagnetics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Summary

of

Train The Trainer workshop on TE E&TC 2017 Course For Subject Electromagnetics.

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadgaon, Pune from **5th to 10th June 2017**. During Workshop revised Third year syllabus and contents were discussed for **Electromagnetics**.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. **Kharat** from SITS Narhe, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by **R.A Patil of RMD Pune** on the second unit which was based on capacitance and boundary conditions..

Third day we discussed about on 3rd Unit which consist Magnetostatics

The unit was discussed by **Prof.Dr R.O Rajankar**, from SCOE who not only explained the concept, method but also explained the application based on the unit which need to discussed with students.

On the fourth day, Session started with discussion on 4th Unit which is based Dynamic electromagnetic fields. The speaker for the session was **Prof. Ingawale Smita** from SKNCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Fifth day we discussed about on 5th Unit which consist of Transmission lines. The unit was discussed by me and Sixth day Prof Mayur has taken concepts of uniform plane waves and snells law.

Overall the workshop was a successful one.

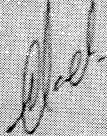
Ms. R.B. Kakkeri
Assistant Professor
SAE, E&TC





Sinhgad Institutes

TRAIN THE TRAINER
T.E. E&TC
Certificate

Mr/Ms Suvarna phule of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Micro controllers, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
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SINHGAD ACADEMY OF ENGINEERING

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S. No. 40/4 A. Near octroi Post, Kondhwa –Saswad Road, Pune – 411048.
☎ : (020) 26934441, 26934550, Fax : 020- 26934297,
E-mail : saeprincipal@sinhgad .edu, Website : www.sinhgad.edu

Department of Electronics and Telecommunication

Summary of Train The Trainer workshop on TE E&TC 2017 Course For Subject Microcontrollers.

The above said workshop was organized at Sinhgad College of Engineering (SCOE), Vadagaon, Pune from 5th to 10th June 2017. During Workshop revised Third year syllabus and contents were discussed for **Microcontrollers**.

One day for each unit was dedicated for a whole unit, so it was a 6 days' workshop.

During the First day, Prof. **Dr. Dnyaneshwar S Mantri** from Sinhgad institution of Technology, Lonavala, highlighted the first unit syllabus and gave understanding about which bit need to be stressed and also explained about how to start the topic so as to make subject interesting for electronics students.

Second day was conducted by **Prof. H. N.Lokhande** from NBN Sinhgad school of Pune on the second unit. Second Unit is based on Interfacing of LEDS, LCD, Keypad, 7 – segment Multiplexed Display, ADC 0809, Programming Environment: Study of software development tool chain (IDE), hardware debugging tools (timing analysis using logic analyser) and different programs based on 8051.

Third day we discussed about on 3rd Unit which consist of Parallel Port Interfacing-II. The unit was discussed by **Prof. Kirti Yadav** from SKNSITS, Lonavala, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students.

On the fourth day, Session started with discussion on 4th Unit which is based PIC Microcontroller Architecture. The speaker for the session was **Prof. R. R. Deshmukh** from SCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Fifth day we discussed about on 5th Unit which consist of Real World Interfacing Part I (Based on PIC Microcontroller). The unit was discussed by **Prof. S. A. Panwar** from SKNCOE, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Sixth day we discussed about on 6th Unit which consist of Real World Interfacing Part II Basics of Serial Communication Protocol: Study of RS232, RS 485, I2C, SPI, MSSP structure (SPI & I2C), UART, Sensor interfacing using ADC, RTC (DS1306) with I2C and EEPROM with SPI. Design of PIC test Board, Home protection System: All programs in embedded C. The unit was discussed by **Prof. J. A. Bagban** from SAE, Kondhwa, who not only explained the concept, method but also explained the application based on the unit which need to discussed with students

Workshop ended where participation certificates were given to all the participants.



Ms. S. S. Phule
Assistant Professor
SAE, E&TC





Sinhgad Institutes


TRAIN THE TRAINER T.E. E&TC

Certificate

Mr/Ms P. R. Shahane of SAE
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Electronic System Design, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE



Sinhgad Technical Education Society's

SINHGAD ACADEMY OF ENGINEERING

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Department of Electronics and Telecommunication

Summary of Train The Trainers Program-TE E&TC/Elex 2015 Curriculam

The above said workshop was organized at Sinhgad college of Engineering Vadgaon ,Pune from 5th to 9th June 2017. The objective of this workshop was to orient all the faculty towards the revised syllabus of TE(E&TC/Elex) and to bring in uniformity in teaching across all the colleges under the STES of Pune.

On the first day session started with Inauguration Function. The First session was based on first module which is based on SMPS system design, so all design aspects are considered and one complete design is finalized.

Second day Session was on the second module i.e. design of Data acquisition system (DAS)

Third day we discussed about on third module which consist of SQL server and the concept of the Database management system (DBMS)

On the fourth day, Session started with discussion on fourth module which is design of communication systems.

On the fifth/last day, Session started with discussion on fifth module which is PCB manufacturing technique.

This way all five modules were discussed in five days

On last day the concluding ceremony was organized .Prof. M. M. Jadhav Coordinator of the program. The feedbacks from all the participants were collected.

The TTT Program was found to be very informative and use full for the Engineering faculties.

B. S. Shahane
24/7

Mrs. P.R.Shahane
Assistant Professor,
E&Tc Dept,
SAOE,Kondhwa




Sinhgad Institutes


TRAIN THE TRAINER T.E. E&TC

Certificate

Mr/Ms M. H. NAIKWADI of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital Communication, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE

A Report On

Faculty Orientation Workshop on "Digital Communication T.E-E&TC revised Syllabus 2015 Course was organized by Sinhgad College of Engineering, Pune during June 05 to June 10, 2017.

Objective:

The objective of this workshop was to orient all the faculty towards the revised syllabus of T.E-E&TC Digital Communication and to bring in uniformity in teaching across all the colleges under the University of Pune

At the end of this course the students will

1. To prepare mathematical background for communication signal analysis.
2. To understand and analyze the signal flow in a digital communication system.
3. To analyze error performance of a digital communication system in presence of noise and other interferences.
4. To understand concept of spread spectrum communication system.

On the first and second day it was decided that At the end of this first unit student must be able to achieve following objectives

- 1) Understand working of waveform coding techniques and analyse their performance.
- 2) Analyze the performance of a baseband and pass band digital communication system in terms of error rate and spectral efficiency.
- 3) Perform the time and frequency domain analysis of the signals in a digital communication system.
- 4) Design of digital communication system.
- 5) Understand working of spread spectrum communication system and analyze its performance.

Day One: Unit Objectives: Dr Achala Deshmukh of SCoE discussed contents of Unit 1

Unit Objectives: On completion the students will be able to

- viii. Draw block diagram of Digital Communication System
- ix. Explain Sampling process
- x. Explain Quantization and Companding
- xi. Explain different Digital Modulation schemes
- xii. Understand LPC

Sinhgad Technical Education Society's
Sinhgad Academy of Engineering, Kondhwa (Bk), Pune.
Department Of Electronics & Telecommunication Engineering

Unit Objectives: On completion the students will be able to

- i. Explain the pseudo noise sequences
- ii. Sampling Theorem and Nyquist criteria
- iii. Types of spread spectrum
- iv. Explain Jamming

Explain frequency hop spread spectrum



Prof M. H. Naikwadi



Sinhgad Institutes

TRAIN THE TRAINER

T.E. E&TC

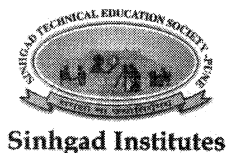
Certificate

Mr/Ms Ashok Joglekar of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital communication, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE



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Department of Electronics and Telecommunication

Six Day Train The Trainer Program (05 to 10 Jun 2017)

This was a six day program for training the trainers for revised DC syllabus at SCOE for 6 days between 05 June 2017 to 10 Jun 2017. The participants were all the teachers who would be teaching the Revised Syllabus for DC from 2017 from all STES colleges.

Each day was devoted to teaching one unit of the revised syllabus of DC for 2017 onwards. There were resource persons from different colleges of STES and I was a resource person for Unit III. Every day was broken into 3 sessions. The first two sessions were devoted to the teaching of the theory and tutorials for the particular unit. The third session was devoted to carrying out the laboratory practical associated with the particular unit.

The workshop provided valuable insights to teaching methodologies which were useful especially for teachers who were taking the subject for the very first time.


Ashok Joglekar



Sinhgad Institutes

TRAIN THE TRAINER

T.E. E&TC

Certificate

Mr/Ms V. V. Joshi of SAE Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital signal processing, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

A Report On

Train the trainer for TE (E&TC) revised syllabus 2017.

DIGITAL SIGNAL PROCESSING

A train the trainer on TE (E&TC) Revised Syllabus 2017 Course for the subject Digital Signal Processing was organized by STES, Pune between 05th to 11th June 2017.

Objective:

The objective of this workshop was to orient all the faculty towards the revised syllabus of TE (E&TC) and to bring uniformity in teaching across all the colleges under Sinhgad group.

DAY-1: 05/06/2017.

Dr. Vandana Rohakale conducted first two sessions on DSP Preliminaries and applications. She covered topic sampling theorem in time domain, representation of signals as vectors, concept of Basis function and orthogonality, Eigen values, Eigen vector, Basic elements of DSP and its requirements, advantages of Digital over Analog signal processing. In the last session of the day she covered matlab program on sampling for different conditions.

DAY-2: 06/06/2017.

Dr. S A Ganorkar conducted second day. In first two sessions topics which he covered are DTFT, DFT and its Properties, circular convolution, linear convolution, Computation of linear convolution using circular convolution, FFT, decimation in time and decimation in frequency using Radix-2 FFT algorithm, Linear filtering using overlap add and overlap save method, Introduction to Discrete Cosine Transform. In the last session all the matlab programs based on DFT are discussed.

DAY-3: 07/06/2017.

Third day session is taken by Mrs deepali Newaskar. She covered Need for transform, relation between Laplace transform and Z transform, between Fourier transform and Z transform, Properties of ROC and properties of Z transform, Relation between pole locations and time domain behavior, causality and stability considerations for LTI systems, Inverse Z transform, Power series method, partial fraction expansion method, Solution of difference equations. Matlab program is discussed in the last session.

Sinhgad Technical Education Society's
Sinhgad Academy of Engineering, Kondhwa (Bk), Pune.
Department Of Electronics & Telecommunication Engineering

DAY-4: 08/06/2017

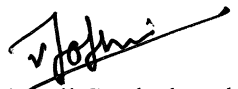
On day 4 session is delivered by Mrs Pradnya Kambale. She covered concept of analog filter design (required for digital filter design), Design of IIR filters from analog filters, IIR filter design by approximation of derivatives, IIR filter design by impulse invariance method, Bilinear transformation method, warping effect. Characteristics of Butterworth filters, Chebyshev filters and elliptic filters, Butterworth filter design, IIR filter realization using direct form, cascade form and parallel form, Finite word length effect in IIR filter design.

DAY-5: 09/06/2017

Mrs. Nandini dhole covered FIR filter unit 5 of DSP. In that she delivered Ideal filter requirements, Gibbs phenomenon, windowing techniques, characteristics and comparison of different window functions, Design of linear phase FIR filter using windows and frequency sampling method. FIR filters realization using direct form, cascade form and lattice form, Finite word length effect in FIR filter design. At the end of day Matlab program is discussed.

DAY-5: 10/06/2017

Last day session is taken by Mrs. Shamiksha Jog. She discussed overview of DSP in real world applications such as Digital crossover audio systems, Interference cancellation in ECG, Speech coding and compression, Compact disc recording system, Vibration signature analysis for defective gear teeth, Speech noise reduction, two band digital crossover.


Mrs. Vaishali Sardeshmukh
Subject Incharge.



Sinhgad Institutes

**TRAIN THE TRAINER
T.E. E&TC**

Certificate

Mr/Ms A. K. Patel. of SAE Kondhwa.
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Digital signal processing, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.

Prof. M. M. Jadhav
Coordinator

Dr. M. B. Mali
Head- E & TC

Dr. S. D. Lokhande
Principal, SCOE

AReportOn

Train the trainer for TE (E&TC) revised syllabus 2017.

DIGITAL SIGNAL PROCESSING

A train the trainer on TE(E&TC) Revised Syllabus 2017 Course for the subject Digital Signal Processing was organized by STES, Pune between 05th to 11th June 2017.

Objective:

The objective of this workshop was to orient all the faculty towards the revised syllabus of TE(E&TC) and to bring in uniformity in teaching across all the colleges under Sinhgad group.

DAY-1: 05/06/2017.

Dr. Vandana Rohakale conducted first two sessions on DSP Preliminaries and applications. She covered topic sampling theorem in time domain, representation of signals as vectors, concept of Basis function and orthogonality, Eigen values, Eigen vector, Basic elements of DSP and its requirements, advantages of Digital over Analog signal processing. In the last session of the day she covered matlab program on sampling for different conditions.

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Sinhgad Technical Education Society's
Sinhgad Academy of Engineering, Kondhwa (Bk), Pune.
Department Of Electronics & Telecommunication Engineering

DAY-4: 08/06/2017

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DAY-6: 10/06/2017

Last day session is taken by Mrs. Shamita Jog. She discussed overview of DSP in real world applications such as Digital crossover audio systems, Interference cancellation in ECG, Speech coding and compression, Compact disc recording system, Vibration signature analysis for defective gear teeth, Speech noise reduction, two band digital crossover.

Ankita Patel







Sinhgad Institutes


TRAIN THE TRAINER T.E. E&TC

Certificate

Mr/Ms C. G. Patil of SAE, Kondhwa
_____ has attended Train The Trainer Program on T.E. (E&TC) for the
subject Electromagnetics, conducted at Department of
Electronics & Telecommunication Engineering, Sinhgad College of Engineering,
Vadgaon (Bk), Pune during June 05 - 10, 2017.


Prof. M. M. Jadhav
Coordinator


Dr. M. B. Mali
Head- E & TC


Dr. S. D. Lokhande
Principal, SCOE

Train the Trainer on

T.E.(E&TC)

05th -10th June 2017

Summary

The teacher training program "Train the Teacher" is conducted to provide training to the teachers and to maintain the uniformity in the teaching across all the Institutes under the umbrella of Sinhgad Technical Education Society.

For the subjects to be taught in T.E.(E&TC), the program was organized by Department of Electronics and Telecommunication Engineering, Sinhgad College of Engineering, Vadgaon, Pune during 05th to 10th June 2017.

The training was organized daily in 3 sessions. Every day one Unit was discussed by various Expert Faculty members from various Institutes like 1.Sinhgad College of Engineering, 2.SKN College of Engineering, 3.RMD COE, 4.SIT Lonavala, 6.SKNSIT Lonavala, etc.

Morning 2 sessions were reserved for discussion on theory part of the syllabus and evening session was reserved for Numerical and practical applications of the concepts. The overall discussion was consisting of the problems faced by the students and solutions to be given to the students.

On last day the workshop was concluded. The required subject materials like presentations, notes, problems books etc. were distributed for the actual implementation.



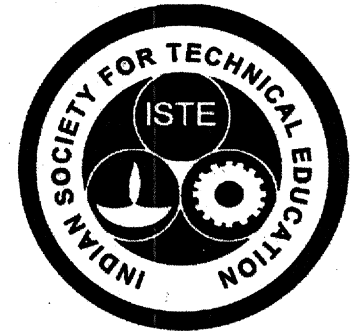
- Dr.C.G.Patil

Sant Dnyaneshwar Shikshan Sanstha's
Annasaheb Dange College of Engineering & Technology, Ashta

(An Autonomous Institute)



ND CET



ISTE Approved One Week Short Term Training Program on
NetSim V10: A Practical Approach


Certificate

This is to certify that Dr./~~Mr.~~/Ms./Mrs. Gaikwad Kaustubh Manikrao of
..... has attended the **ISTE** Approved One Week Short Term Training Program
on "**Netsim V10: A Practical Approach**" at Department of Electronics and Telecommunication Engineering,
Annasaheb Dange College of Engineering & Technology, Ashta, Sangli during 11th - 15th December 2017.


Prof. M.M. Kumbhar
Coordinator


Prof. Ms. A.A. Pujari
Co-Coordinator


Prof. Mrs. S.S. Shinde
Convener


Dr. N.D. Sangle
Dean (Academic)


Dr. L.V. Waghmode
Registrar


Prof. R.A. Kanai
Executive Director



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Department of Electronics and Telecommunication

Summary of

ISTE Approved One Week Short Term Training (STTP)

On

"Netsim V10: A Practical Approach"

For One week at Annasaheb Dange College of Engineering & Technology, Ashta

Short term training Program on "NetSim V10: A Practical Approach" subject was conducted at Dept of E&TC ADCET, Ashta, Sangli approved by ISTE during 11th to 15th December 2017.

This Faculty oriented Short term training program was focused on Practical Approach of NetSim V10 software and its scope in Computer Networking so as to build different network and implementation of various communication protocols along with the recent advances in networking.

Some eminent personalities such as Dr.Pandyaji, Mr. Kanangraj from NetSim Company enlighten us with their knowledge and encouraged us to do research in Computer Networking using NetSim V10 approach in coming time.

This workshop really helped faculty for the understanding about recent trends in Computer Networking Using NetSim V10 and implementing various communication protocols and as well as how the topics can be used for further research studies.

Dr.Kaustubh M Gaikwad
Electronics and Telecommunication dept,
SAE,Kondhwa



Sinhgad Institutes

Celebrating 25 Years
OF ACADEMIC EXCELLENCE

Sinhgad Technical Education Society's
SINHGAD COLLEGE OF ENGINEERING

(Accredited by NAAC with "A" Grade)

Survey No. 44/1, Off Sinhgad Road, Vadgoan (Bk), Pune-411041, Maharashtra, India

Certificate

One Week Faculty Development Programme on
Image Processing-Advances & Research Challenges

This is to certify that Mr./ ~~Ms.~~/ ~~Mrs.~~ Sanket Anil Bankar
has attended IEEE and MathWorks approved one week Faculty Development Program on "Image Processing-Advances & Research Challenges" organized by Department of E&TC Engineering, Sinhgad College of Engineering, Pune, during December 11-15, 2017.

Dr. S. R. Ganorkar
Coordinator

Dr. M. B. Mali
Convener

Dr. V. V. Dixit
Vice-Principal

Dr. S. D. Lokhande
Principal



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E-mail : saeprincipal@sinhgad .edu, Website : www.sinhgad.edu

Department of Electronics and Telecommunication

One Week Faculty Development Program (11-15 December 2017)

on Image Processing-Advances & Research Challenges

This was a Faculty Development Program held at Sinhgad College of Engineering (SCOE) Pune from **11-15 December 2017** organized by SCOE for all faculties from SPPU for the subject **Image Processing-Advances & Research Challenges** in collaboration with MathWorks and IEEE Communication Society (IEEE ComSoc).

Main purpose of this FDP was to make aware all the faculties about new areas and challenges available in the field of Image Processing. Same field can be chosen for further education of PhD.

Many fields from Image Processing were discussed like Image Processing for Bio-medical Applications, Fuzzy Logic and Deep Learning etc. Demo of Matlab version 2017 was also given and it's amazing features were introduced to the audience.

Mr. S. A. Bankar

SAE Kondhwa

Bansilal Ramnath Agarwal Charitable Trust's

Vishwakarma Institute of Technology

(An Autonomous Institute Affiliated to Savitribai Phule Pune University)

666, Upper Indira Nagar, Bibwewadi, Pune -37

One Week Workshop

On

"Deep Learning"

CERTIFICATE

This is to certify that Dr./Mr./Ms./Mrs. Rushikesh Borse

has attended workshop on "Deep Learning" during JULY 18th - 22nd, 2017 at the
Department of Information Technology & MCA and Computer Engineering,
Vishwakarma Institute of Technology, Pune.

Convener

Prof. Dr. Premchand Ghadekar

Convener

Prof. Dr. Rajesh Isnekar

Director

Workshop attended Summary

Workshop: One week STTP on Deep Learning (DL)

Date: 18 to 22th July 2017

Venue: VIT, Bibwewadi, Pune 37

As Machine learning is a buzz word now a days, so do Deep Learning. Since Machine learning has many applications in signal and image processing domain, I was inspired to attend this workshop. I learned various aspects in these 5 days:

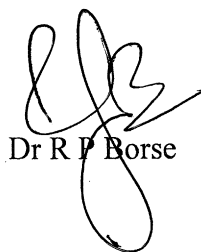
Day 1: Dr. Ketan Bhutaker: He talked about basics of machine learning and how it differed from traditional learning. We also had hands on in the afternoon. We also installed Software: Anaconda on worked on 2-3 algorithms.

Day 2 and Day 3: Dr Satish Patil who is Founder and Chief Data Scientist, Lemoxo Technologies. Pune. Discussed about supervised and unsupervised learning approaches of ML. He gave industry insights on various issues of ML. On Day 3 we have Mr Anand, we discussed about Convolutional Neural network aspect on ML

Day 4 and Day 5: Dr Mansi Patwardhan. These 2 days were the best days of the workshop. He started with a time-temperature data plot. Further derived the full mathematics and came to the concept of weight and bias of NN. Also discussed the multi-level perceptron model. The concept of linear convex optimization was explain the help of some lab assignments. On Day 5 she talked about the LSNN aspect of DL. She ended with lab assignment on sentimental analysis problem based on text data.

Deep learning is a vast ocean. A lot needs to be explored more. I really understood the concept of convolutional neural network, which is a imp stage of Deep learning.

I am thankful to my HoD for allowing me to attend this important workshop. Also thankful to all those, we adjusted my load in this duration.



Dr R P Borse



Sinhgad Technical Education Society's
SINHGAD INSTITUTE OF TECHNOLOGY,
LONAVALA-410401



Sinhgad Institutes


Department of Computer Engineering

CERTIFICATE OF PARTICIPATION

This is to certify that Dr./Mr./Ms. Gai Kwad Kaustubh Manikrao has successfully attended One Week of _____

Faculty Development Program on "**Data Science with Python**" organized by Department of Computer Engineering, SIT, Lonavala held during **04th to 8th October, 2017.**


Prof. S. B. WAYKAR
Coordinator


Prof. S. R. PATIL
Convener


Dr. S. D. BABAR
H.O.D. (Comp.)


Dr. M. S. GAIKWAD
Principal



Sinhgad Institutes

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Department of Electronics and Telecommunication

Summary of

Faculty Development Program (FDP)

On

"Data Science with Python"

For One week at Sinhgad Institute of Technology, Lonavala

Faculty development Program on "Data Science with Python" subject was organized by Dept of Computer Engineering, SIT, Lonavala during 04th to 08th October 2017.

This Faculty oriented training program was focused on implementation and recent trends in Data Science with Python and its practical hands on were arranged to understand the scope of Data Science in Data Analysis and embedded system applications.

Some eminent personalities explored the use of libraries such as Pandas Matplotlib to explore and analyze the data with python and its relevance in today's technology world. Their knowledge encouraged us to work in Data Science with python.

This workshop really helped faculty for the understanding about recent trends in Data Science with Python and use of different libraries such as Numpy and Pandas to explore and analyze data and as well as how the topics can be used for further research studies.

Dr. Kaustubh M Gaikwad
Electronics and Telecommunication dept,
SAE, Kondhwa