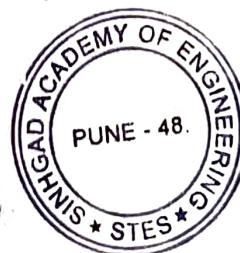


	SEA	TEA	TEB	TEC	BEA	BE B	BEC	PG VLSI-I	PG VLSI -III	PhD	
1 8:00 8:00	SVT DSA	PRS ETL	JAB MCA								
2 8:15 9:45	ED CP I	MMP MCA	MC ES AP ED Pr		ABI CNS	DKK EL-I BE EMB					
3 9:50 10:20	SB P RJ	KNJ MECH	DC Pr BS JA PR B S	Dr. CGP ETL TT	VMS RMT	Ms. PSD EL-2 EPD	RKP RMT	ABI VLSI1 DCD	VDB TVVC		
4 10:25 12:15	SAS RJ	RKP DC	RBK ETL	ME ES CH ED Pr KN CG J P	TSL VLSI	RM VL CN S Pr Pr Pr JP MM RP S P R	BSG EL-2 EPD	DKK DS1-VLSI1 ESD	ABI DS VLSI 3_EL-III		
5 12:15 1:15	TSL DE	VMS DSP	RUY MECH		PRS EL-2 EPD		SVT EL-I BE EMB	VDB DS1 VLSI1 RM	Dr. KMG ASICD		
6 1:30 1:30											
7 2:15 3:15	GUEV	ES ED Pr Pr PS MM RK D P	VDB DSP	JAB MCA	VL SI Pr Pr TS ST L	R MT Pr Pr Ms. P ABI RJ	CN S Pr Pr DI P Pr	GUEV	RPR CNS	Dr. KMG VLSI1 RC	
8 3:15 4:15			BSG DC	KNJ MECH				RBK VLSI	RUY VLSI1 EL-1 WSN	Project	
9 4:30 5:30				Ms. PSD DSP							
10 5:30 6:30				Ms. STSP DC							
11 6:30 7:30											
12 7:30 8:30											

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Sinhgad Technical Education Society's  
Sinhgad Academy of Engineering, Kondhwa, Pune-411048

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
ACADEMIC YEAR 2019-2020 SEM -I

w.e.f. 28/06/2019

		SEA	TEA	TEB	TEC	BEA	BE B	BEC	PG VLSI-I	PG VLSI-III	PhD
<div> <div>Thursday</div> <div>Friday</div> <div>Tuesday</div> </div>	1 8:30 9:00	SVT DSA	VMS DSP	BSG DC							
	2 9:30 10:00	RJ EIT		RBK ETL							
	3 10:30 11:00	TSL DE	ME ES MC CH ED AP Pr Pr Pr KN PR JA J S B	BSG ESED	Dr. CGP ETL	VMS RMT	VL CN EM SI S BD Pr Pr Pr MM SB SV P P T	RPR CNS	DKK DS1-VLSI ESD	ABI DS VLSI 3_EL-III	
	4 11:30 12:15	JPS ECM	RKP DC	RUY MECH			SBP CNS	RBK VLSI	ABI VLSI1 DCD	VDB TVVC	
	5 12:45 1:15	SBP EDC	PRS ETL	VDB DSP	ES DC ED Pr Pr MC BS ST G SP	CN VL DI R S SI P AT Pr Pr Bi Pr RP TS CG R L P R	MMP VLSI	SVT EL-1 BE EMB	RUY VLSI1 EL-1 VSN	Dr. KMG ASICD	
	6 1:30 2:00										
	7 2:15 2:45	DE Pr	ED CP Pr	ME CH Pr Pr RU BS Y G	Ms. STSP DC	Dr. CGP EL-1 BE DIP	Ms. PSD EL-2 EPD	VL RM SI T Pr Pr RS RK K P	DS1 VLSI1 RM		
	8 3:15 3:45	TS L	SB P		KNJ MECH	ABI CNS	JPS RMT		DS1 VLSI1 RM		
	9 4:30 5:00				Ms. PSD DSP						
	10 5:30 6:00				Ms. STSP ESED						
	11 6:30 7:00										
	12 7:30 8:00										

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Sinhgad Academy of Engineering, Kondhwa, Pune-411048

DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING  
ACADEMIC YEAR 2019-2020 SEM -I

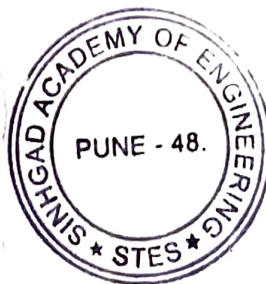
w.e.f. 28/06/2019

	SEA	TEA	TEB	TEC	BEA	BE B	BEC	PG VLSI-I	PG VLSI-III	PhD
1	RJ SAS	RKP ESED								
2	DS EC AP M r Pr	MC DS ES AP PP ED r r Pr	RBK ETL		ABI CNS			Dr. KMG VLSI RC		
3	SV RU T Y	MM VM JA P S B	BSG DC	Ms. PSD DSP	TSL VLSI	GN EM RM S BD T Pr Pr Pr SB DK RK P K P	RBK VLSI	VDB DS1 VLSI RM	Dr. KMG ASICD	
4	JPS ECM	VMS DSP	VDB DSP	DS MC PP AP V S PR D S	DI CN RM VL P S T SI Pr Pr Pr RP ST RB R R SP K	EL-1 BE EMB	SVT EL-1 BE EMB	RUY VLSI EL-1 WSN	DS VLSI 3_EL-III	ABI
5	SBP EDC	KNJ MECH	JAB MCA			JPS RMT	RKP RMT	DS1-VLSI ESD	VDB TVVC	
6										
7	SVT DSA	MMP MCA	MC ME DS AP CH PP r Pr r	Dr. CGP ETL				ABI VLSI DCD		
8	TSL DE	PRS ETL TT	JA RU VD B Y B	KNJ MECH			RM EM T BD Pr Pr JP PS S D	Dr. KMG VLSI RC		
9				Ms. STSP DC						
10										
11										
12										

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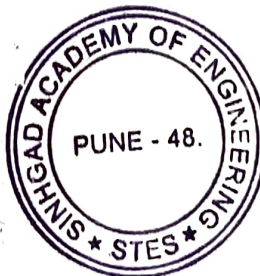


		SEA	TEA	TEB	TEC	BEA	BE B	BEC	PG VLSI-I	PG VLSI -III	PhD
Thursday	1 0:00 0:00	SBP EDC	MMP MCA	RUY MECH							
	2 0:00 0:00	RJ SAS 11		ES DS ME ED PP CH Pr r Pr		PRS EL-2 EPD					
	3 0:00 0:00	JPS ECM		ES DS ME ED PP CH Pr r Pr	JAB MCA	Dr. CGP EL-1 BE DIP	MMP VLSI	DE Project Part I	DKK DS1-VLSI ESD	ABI DS VLSI 3_EL-III	
	4 0:00 0:00	STR-J	RKP ESED	STR-J	DCP Y	DCP Y	DKK EL-1 BE EMB	BSG EL-2 EPD	RUY VLSI1 EL-1 WSN	VDB TVVC	
Friday	5 0:00 0:00	STR-J	VMS DSP	STR-J	DCP Y	DCP Y	MMP VLSI	RKP RMT	ABI VLSI1 DCD	Dr. KMG ASICD	
	6 0:00 0:00										
	7 0:00 0:00	EI ST Pr	DE Pr TS L	ME DS CH PP Pr r	BSG ESED		JPS RMT	EM CN ED S Pr Pr PS RP Dr R	Dr. KMG VLSI1 RC	ABI DS VLSI 3_EL-III	
	8 0:00 0:00	RJ	DC Pr Mg STP P	ME DS CH PP Pr r	JAB MCA		SBP CNS		RUY VLSI1 EL-1 WSN		
	9 0:00 0:00				Ms. PSD DSP						
	10 0:00 0:00										
	11 0:00 0:00										
	12 0:00 0:00										

  
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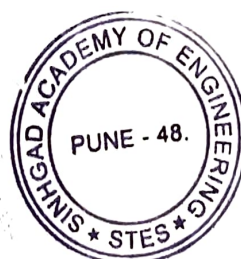


	SEA	TEA	TEB	TEC	BEA	BE B	BEC	PG VLSI-I	PG VLSI-III	PhD
Friday	1 8:30-9:30 DSA	SVT	RBK ETL TT							
	2 9:30-10:30 DC	RKP	DC						DKK Project	
	3 10:30-11:30 MECH	KNJ	DC	Di CGP ETL	PRS EL-2 EPD	SBP CNS	RPR CNS		DKK Project	
	4 11:30-12:30 SAS	RJ DC	RKP DC	VDB DSP	M ME CACH Pr Pr	VMS RMT	Ms. PSD EL-2 EPD	CN VL S SI Pr Pr	VLSI Lab Practical	Project
	5 12:30-1:30 ECM	JPS ETL	PRS DC	BSG DC	ABI CNS	SBP CNS	AP RB R K			
	6 1:30-2:30									
Friday	7 2:30-3:30 EDC	SBP		Ms. STSP ESED	TSL VLSI	EM R VL BD MT SI Pr Pr Pr	BSG EL-2 EPD		VLSI Lab Practical	Semester
	8 3:30-4:30 DE	TSL	DS DC ME PP Pr CH Pr Pr	JAB MCA	Dr. CGP EL-I BE DIP	SV JP MM T S P	RPR CNS			
	9 4:30-5:30			Ms. STSP DC						
	10 5:30-6:30									
	11 6:30-7:30									
	12 7:30-8:30									

  
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Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

Sinhgad Institutes

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

School Name

SEA

	Monday	Tuesday	Wednesday	Thursday	Friday
1 8:00 - 9:00	CR- 215 DSA SVT	CR- 215 DSA SVT	CR- 215 SAS RJ	CR- 215 EDC SBP	CR- 215 DSA SVT
2 9:00 - 10:00	SA1 SA2 EDCPr EI&TPr	CR- 215 EIT RJ	SA1 SA2 DSAPr ECM Pr VLSI CE LAB LAB 1 227 SVT 229 RUY	CR- 215 SAS TT RJ	SA1 SA2 ECM Pr DSAPr PROJ EMBD RMT LAB 205 RUY 210 SVT
3 10:00 - 11:00	SDC CE LAB LAB 1 202 SBP 229 RJ	CR- 215 DE TSL		CR- 215 ECM JPS	
4 11:15 - 12:15	CR- 215 SAS RJ	CR- 215 ECM JPS	CR- 215 ECM JPS	STP -I	CR- 215 SAS RJ
5 12:15 - 1:15	CR- 215 DE TSL	CR- 215 EDC SBP	CR- 215 EDC SBP		CR- 215 ECM JPS
7 2:15 - 3:15	GL/EV	SA1 SA2 DEPr EDCPr BASIC SDC LAB LAB 211 TSL 202 SBP	CR- 215 DSA SVT	SA1 SA2 EI&TPr DEPr CE BASIC LAB LAB 229 RJ 211 TSL	CR- 215 EDC SBP
8 3:15 - 4:15		CR- 223	CR- 215 DE TSL		CR- 215 DE TSL

Teachers:

JPS Dr.JP Shinde SBP S B Pawar  
RJ RJain SVT S V Thakur  
RUY RU Yawle TSL T S Londhe

Subjects:

DE DE EDCPr EDCPr  
DEPr DEPr EITPr EITPr  
DSA DSA EIT EIT  
DSAPr DSAPr GL/EV Guest Lecture /Event  
ECM Pr ECM Pr SAS TT SAS TT  
ECM ECM SAS SAS  
EDC EDC STP -I STP -I

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Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

TEA

School Name	Monday	Tuesday	Wednesday	Thursday	Friday
1 8:00 - 9:00	CR- 223 ETL PRS	CR- 223 DSP VMS	CR- 223 ESED RKP	CR- 223 MCA MMP	
2 9:00 - 10:00	CR- 223 MCA MMP	TEA1 TEA2 TEA3 MECH Pr POWE R LAB 233 KNJ ESED Pr DSP LAB 228 PRS MCAPr MMA LAB 209 JAB	TEA1 TEA2 TEA3 MCAPr DSPPr ESED Pr MMA DSP SDC LAB 209 MMP LAB 228 VMS LAB 202 JAB	CR- 223 STP-III	CR- 223 DC RKP
3 10:00 - 11:00	CR- 223 MECH KNJ			CR- 223	CR- 223 MECH KNJ
4 11:15 - 12:15	CR- 223 DC RKP	CR- 223 DC RKP	CR- 223 DSP VMS	CR- 223 ESED RKP	CR- 223 DC RKP
5 12:15 - 1:15	CR- 223 DSP VMS	CR- 223 ETL PRS	CR- 223 MECH KNJ	CR- 223 DSP VMS	CR- 223 ETL PRS
7 2:15 - 3:15	TEA1 TEA2 TEA3 ESED Pr EMBD LAB 210 PSD MCAPr MMA LAB 209 MMP DCP R LAB 223 RKP	GL/EV	CR- 223 MCA MMP	TEA1 TEA2 TEA3 DCPr MECH Pr POWE R LAB 233 KNJ DSPPr DSP LAB 228 VMS	TEA1 TEA2 TEA3 DSPPr DCP R LAB 228 VMS DCP R LAB 223 RKP MECH Pr POWE R LAB 233 KNJ
8 3:15 - 4:15		CR - 231	CR- 223 ETL TT PRS		

Timetable generated:22-Jul-19

aSc Timetables

Teachers:

Subjects:

JAB JA Bagban Ms. STSP Ms. ST Sawant-Patil  
KNJ KN Jagtap PRS PR Shahane  
MMP MM Patil RKP Dr. RK Patil  
Ms. PSD Ms. P S Deshpande VMS V M Sardeshmukh

DC DC  
DCPr DCPr  
DSP DSP  
DSPPr DSPPr  
ESED FESED Pr  
ESED ESED  
ETL TT ETL TT

ETL ETL  
GL/EV Guest Lecture /Event  
MCA MCA  
MCAPr MCAPr  
MECH FEL-I Mechatronics Pr  
MECH EL-I Mechatronics  
STP-III STP-III

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Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

TEB

Sinhgad Institutes

	Monday	Tuesday	Wednesday	Thursday	Friday
1 8:00 - 9:00	CR- 224 MCA JAB	CR- 224 DC BSG		CR- 224 MECH RUY	CR- 224 ETL TT RBK
2 9:00 - 10:00	TEB1 TEB2 TEB3 DCPr MCAPr ESED Pr MMA DSP LAB LAB 2 230 BSG 209 JAB 228 PRS	CR- 224 ETL RBK	CR- 224 ETL RBK	TEB1 TEB2 TEB3 ESED Pr MMA DSP LAB 209 RBK DSPPr LAB 228 VDB MECH Pr POWE R LAB 233 RUY	TEB1 TEB2 TEB3 DSPPr ESED Pr MMA DSP LAB 228 VDB DCPr CE LAB 2 230 BSG 209 JAB 228 PRS
3 10:00 - 11:00	CR- 224 ETL RBK	CR- 224 MECH RUY	CR- 224 DC BSG		CR- 224 DSP VDB
4 11:15 - 12:15	CR- 224 MECH RUY	CR- 224 DSP VDB	CR- 224 MCA JAB	CR- 224 STP-III	CR- 224 DC BSG
5 12:15 - 1:15	CR- 224 DSP VDB	TEB1 TEB2 TEB3 MECH Pr DCPr MCAPr POWE R LAB 233 RUY 2 230 BSG 209 JAB 228 PRS	TEB1 TEB2 TEB3 MCAPr MECH Pr DSPPr MMA LAB 209 JAB 233 RUY 228 VDB	CR- 224 ESED BSG	
6 2:15 - 3:15	CR- 224 DC BSG			CR- 224 MCA JAB	GL/EV
7 3:15 - 4:15					

Timetable generated 23-Jul-19

456 Timetable

Teachers:

BSG B S Gayal RUY RU Yawle  
JAB JA Bagban VDB VD Bharate  
PRS PR Shahane VMS V M Sardeshmukh  
RBK RB Kakkeri

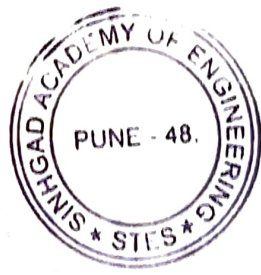
Subjects:

DC DC ETL ETL  
DCPr DCPr GL/EV Guest Lecture /Event  
DSP DSP MCA MCA  
DSPPr DSPPr MCAPr MCAPr  
ESED PESED Pr MECH PEL-I Mechatronics Pr  
ESED ESED MECH EI-I Mechatronics  
ETL TT ETL TT STP-III STP-III

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Sinhgad Institutes

Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

TEC

School Name	Monday	Tuesday	Wednesday	Thursday	Friday
3 10:00 - 11:00	CR- 224 ETL TT Dr. CGP	CR - 231 ETL Dr. CGP	CR- 223 DSP Ms. PSD	CR - 231 MCA JAB	CR- 224 ETL Dr. CGP
4 11:15 - 12:15	TEC1 TEC2 MECH Pr POWE R LAB 233 KNJ	TEC1 TEC2 ESED Pr DSPA LAB 228 BSG	TEC1 TEC2 DSPPr MCAPr DSP MMA LAB 228 PSD 209 PRS	TEC1 TEC2 DCPr DSPPr CE LAB DSP LAB 223 SP 228 PSD	TEC1 TEC2 MCAPr MECH Pr MMA POWE R LAB 209 JAB 233 KNJ
5 12:15 - 1:15					
7 2:15 - 3:15	CR- 215 MCA JAB	CR- 224 DC Ms. STSP	CR- 216 ETL Dr. CGP	STP-III	CR- 224 ESED Ms. ST6P
8 3:15 - 4:15	CR- 215 MECH KNJ	CR- 224 MECH KNJ	CR- 216 MECH KNJ		CR- 224 MCA JAB
9 4:30 - 5:30	CR- 215 DSP Ms. PSD	CR- 223 DSP Ms. PSD	CR- 216 DC Ms. STSP	CR- 216 DSP Ms. PSD	CR- 224 DC Ms. STSP
10 5:30 - 6:30	CR- 215 DC Ms. STSP	CR- 223 ESED Ms. STSP			

Timetable generated 22-Jul-19

aSc Timetables

Teachers:

BSG B S Gayal  
Dr. CGP Dr. CG Patil  
JAB JA Bagban  
KNJ KN Jagtap

Ms. PSD Ms. P S Deshpande  
Ms. STSP Ms. ST Sawant-Patil  
PRS PR Shahane

Subjects:

DC DC  
DCPr DCPr  
DSP DSP  
DSPPr DSPPr  
ESED PESED Pr  
ESED ESED  
ETL TT ETL TT

ETL ETL  
GL/EV Guest Lecture /Event  
MCA MCA  
MCAPr MCAPr  
MECH FEL-I Mechatronics Pr  
MECH EL-I Mechatronics  
STP-III STP-III

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Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

Sinhgad Institutes

BEA

School Name	Monday	Tuesday	Wednesday	Thursday	Friday
2 9:00 - 10:00	CR- 215 CNS ABI		CR - 231 CNS ABI	CR- 216 EL-2 EPD PRS	
3 10:00 - 11:00	CR- 215 RMT VMS	CR- 223 RMT VMS	CR - 231 VLSI TSL	CR- 216 EL-I BE DIP Dr. CGP	CR- 216 EL-2 EPD PRS
4 11:15 - 12:15	CR - 231 VLSI TSL	BEA1 BEA2 BEA3 BEA4 CNS VLSI DIP RMT Pr Pr Pr Pr Resea VLSI EMBD PROJ rch LAB LAB RMT 217RBR 227TSL 210CGP 205RJ	BEA1 BEA2 BEA3 BEA4 DIP CNS RMT VLSI Pr Pr Pr Pr EMBD Resea PROJ VLSI LAB rch RMT LAB 210RJ 217RBR 205TSP 227RBK	BEA1 BEA2 BEA3 BEA4 RMT DIP VLSI CNS Pr Pr Pr Pr PROJ EMBD VLSI PROJ RMT LAB LAB Resea 205RJ 210CGP 227TSL 217RBR	CR- 216 RMT VMS
5 12:15 - 1:15	CR - 231 EL-2 EPD PRS				CR- 216 CNS ABI
7 2:15 - 3:15	BEA1 BEA2 BEA3 BEA4 VLSI RMT CNS DIP Pr Pr Pr Pr VLSI PROJ Resea DSP LAB RMT rch LAB 227TSL 205TSP 217AB 228RJ	CR- 223 EL-I BE DIP Dr. CGP	GL/EV	BE Project Part I	CR- 216 VLSI TSL
8 3:15 - 4:15		CR- 223 CNS ABI	CR - 231	VLSI LAB 227	CR- 216 EL-I BE DIP Dr. CGP

Timetable generated 22-Jul-19

aSc Timetables

Teachers:

ABI AB Ingole RJ RJain  
Dr. CGP Dr. CG Patil RPR R P Rajput  
Ms. STSP Ms. ST Sawant-Patil TSL T S Londhe  
PRS PR Shahane VMS V M Sardeshmukh  
RBK RB Kakkeri

Subjects:

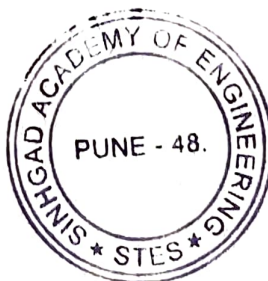
BE ProjBE Project Part I  
CNS Pr CNS Pr  
CNS CNS  
DIP Pr EL-1 BE DIP Pr  
EL-2 EPEL-2 EPD  
EL-I BE EL-I BE DIP

EMBD PEL-I BE EMBD Pr  
GL/EV Guest Lecture /Event  
RMT PrRMT Pr  
RMT RMT  
VLSIPr VLSI Pr  
VLSI VLSI Design Technology

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Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

BE B

Sinhgad Institutes

School Name

	Monday	Tuesday	Wednesday	Thursday	Friday
2 9:00 - 10:00	CR - 231 EL-I BE EMB DKK	BEB1 BEB2 BEB3 VLSI Pr VLSI LAB 227MMP CNS Pr Resea rch 217SBP EMB D Pr EMBD LAB 210SVT	BEB1 BEB2 BEB3 CNS Pr Resea rch 217SBP EMB D Pr EMBD LAB 210SVT RMT Pr PROJ RMT 205RKP	CR - 224 VLSI MMP	CR - 231 CNS SBP
3 10:00 - 11:00	CR - 231 EL-2 EPD Ms. PSD	BEB1 BEB2 BEB3 CNS SBP	CR - 231 EL-I BE EMB DKK	CR - 231 EL-I BE EMB DKK	CR - 231 EL-2 EPD Ms. PSD
4 11:15 - 12:15	BEB1 BEB2 BEB3 RMT Pr PROJ RMT 205JPS VLSI Pr VLSI LAB 227MMP CNS Pr Resea rch 217RBP	CR - 231 VLSI MMP	CR - 231 RMT JPS	CR - 231 VLSI MMP	CR - 231 CNS SBP
5 12:15 - 1:15	GL/EV	CR - 215 EL-2 EPD Ms. PSD	BE Project Part I	CR - 231 RMT JPS	BEB1 BEB2 BEB3 EMB D Pr EMBD LAB 210SVT RMT Pr PROJ RMT 205JPS VLSI Pr VLSI LAB 227MMP
7 2:15 - 3:15	GL/EV	CR - 215 RMT JPS	VLSI LAB 227	CR - 231 CNS SBP	
8 3:15 - 4:15	CR - 231				

Timetable generated 22-Jul-19

asc Timetables

Teachers:

DKK	DK Kamat	RKP	Dr. RK Patil
JPS	Dr. JP Shinde	RPR	R P Rajput
MMP	MM Patil	SBP	S B Pawar
Ms. PSD	Ms. P S Deshpande	SVT	S V Thakur

Subjects:

BE ProjBE Project Part I  
CNS Pr CNS Pr  
CNS CNS  
EL-2 EPEL-2 EPD  
EL-I BE EL-I BE EMB  
EMBD PEL-I BE EMBD Pr

GL/EV Guest Lecture /Event

RMT PrRMT Pr

RMT RMT

VLSIPr VLSI Pr

VLSI VLSI Design Technology

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Sinhgad Institutes

Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

BEC

School Name	Monday	Tuesday	Wednesday	Thursday	Friday
2 9:00 - 10:00				BE Project Part I	CR- 215 CNS RPR
3 10:00 - 11:00	CR- 216 RMT RKP	CR- 216 CNS RPR	CR- 216 VLSI RBK		
4 11:15 - 12:15	CR- 216 EL-2 EPD BSG	CR- 216 VLSI RBK	CR- 216 EL-I BE EMB SVT	CR- 216 EL-2 EPD BSG	BC1 BC2 CNS VLSI Pr Pr Resea VLSI rch LAB 217RBK 227RBK
5 12:15 - 1:15	CR- 216 EL-I BE EMB SVT	CR- 216 EL-I BE EMB SVT	CR- 216 RMT RKP	CR- 216 RMT RKP	
7 2:15 - 3:15	CR- 216 CNS RPR	BC1 BC2 VLSI Pr RMT Pr PROJ LAB RMT 227RBK 205RKP	BC1 BC2 RMT EMB Pr D Pr PROJ EMBD LAB 205JPS 210PSD	BC1 BC2 EMB D Pr CNS Pr PROJ Resea rch 217RBK	CR- 223 EL-2 EPD BSG
8 3:15 - 4:15	CR- 216 VLSI RBK				CR- 223 CNS RPR

Teachers:

BSG B S Gayal RKP Dr. RK Patil  
JPS Dr. JP Shinde RPR R P Rajput  
Ms. PSD Ms. P S Deshpande SVT S V Thakur  
RBK RB Kakkeri

Subjects:

BE Project Part I  
CNS Pr CNS Pr  
CNS CNS  
EL-2 EPD EL-2 EPD  
EL-I BE EMB EL-I BE EMB  
EMBD EL-I BE EMBD Pr

GL/EV Guest Lecture /Event  
RMT Pr RMT Pr  
RMT RMT  
VLSI Pr VLSI Pr  
VLSI VLSI Design Technology

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**DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING**  
**ACADEMIC YEAR 2019-2020 SEM -I**

PG VLSI-I

	Monday	Tuesday	Wednesday	Thursday	Friday
2 9:00 - 10:00			PG CL-2 222 VLSI1 RC Dr. KMG		
3 10:00 - 11:00	PG CL-2 222 VLSI1 DCD ABI	PG CL-2 222 DS1-VLSI1 ESD DKK	PG CL-2 222 DS1 VLSI1 RM VDB	PG CL-2 222 DS1-VLSI1 ESD DKK	
4 11:15 - 12:15	PG CL-2 222 DS1-VLSI1 ESD DKK	PG CL-2 222 VLSI1 DCD ABI	PG CL-2 222 VLSI1 EI-1 WSN RUY	PG CL-2 222 VLSI1 EI-1 WSN RUY	VLSI1_Lab Practice-I
5 12:15 - 1:15	PG CL-2 222 DS1 VLSI1 RM VDB	PG CL-2 222 VLSI1 EI-1 WSN RUY	PG CL-2 222 DS1-VLSI1 ESD DKK	PG CL-2 222 VLSI1 DCD ABI	
7 2:15 - 3:15	PG CL-2 222 VLSI1 RC Dr. KMG	PG CL-2 222 DS1 VLSI1 RM VDB	PG CL-2 222 VLSI1 DCD ABI	PG CL-2 222 VLSI1 RC Dr. KMG	VLSI1_Lab Practice-I
8 3:15 - 4:15	PG CL-2 222 VLSI1 EI-1 WSN RUY	PG CL-2 222 DS1 VLSI1 RM VDB	PG CL-2 222 VLSI1 RC Dr. KMG	PG CL-2 222 VLSI1 EI-1 WSN RUY	

Timetable generated 22-Jul-19

asc Timetables

Teachers:

ABI AB Ingole  
DKK DK Kamat  
Dr. KMG Dr. KM Gaikwad

RUY RU Yawle  
VDB VD Bharate

Subjects:

DS1 VLSI1 RM  
DS1-VLSI1 -VLSI1 ESD  
VLSI1 DVLSI1 DCD

VLSI1 EVLSI1 EI-1 WSN  
VLSI1 RVLSI1 RC  
VLSI1\_LVLSI1\_Lab Practice-I

T T INCHARGE

HOD

PRINCIPAL





Sinhgad Institutes

Sinhgad Technical Education Society's

Sinhgad Academy Of Engineering, Kondhwa (BK) , Pune -48.

DEPARTMENT OF ELECTRONICS & TELECOMM. ENGINEERING

ACADEMIC YEAR 2019-2020 SEM -I

PG VLSI -III

School Name

	Monday	Tuesday	Wednesday	Thursday	Friday
2 9:00 - 10:00					ProjectI VLSI LAB 227 DKK
3 10:00 - 11:00	PG Research 217 A TVVC VDB	PG Research 217 A DS VLSI 3_EL-III ABI	PG Research 217 A ASICD Dr. KMG	PG Research 217 A DS VLSI 3_EL-III ABI	
4 11:15 - 12:15	PG Research 217 A DS VLSI 3_EL-III ABI	PG Research 217 A TVVC VDB	PG Research 217 A DS VLSI 3_EL-III ABI	PG Research 217 A TVVC VDB	ProjectI EMBD LAB 210
5 12:15 - 1:15	PG Research 217 A ASICD Dr. KMG	PG Research 217 A ASICD Dr. KMG	PG Research 217 A TVVC VDB	PG Research 217 A ASICD Dr. KMG	
7 2:15 - 3:15	ProjectI	SeminarII	ProjectI	PG Research 217 A DS VLSI 3_EL-III ABI	SeminarII BASIC LAB 211 Dr. KMG
8 3:15 - 4:15					
	SDC LAB 202	EMBD LAB 210 Dr. KMG	PG Research 217 B DKK		

aSc Timetables

Teachers:

ABI AB Ingole  
DKK DK Kamat

Dr. KMG Dr. KM Gaikwad  
VDB VD Bharate

Subjects:

ASICD VLSI\_3\_ASICD  
DS VLSI\_3\_DS VLSI 3\_EL-III  
ProjectI VLSI\_3\_ProjectI

SeminarVLSI\_3\_SeminarII  
TVVC VLSI\_3\_TVVC

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