



Mr. Vivek Prakash Niwane
M.E. (VLSI & Embedded Systems)
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Academic Qualification:

Degree	Institute Name	University Name	Year of Passing	Qualification Status
M.E.	SCOE, Pune	SPPU	2010	Completed
B.E.	SSBT's COET, Jalgaon	NMU	2006	Completed

Research Expertise

Subject Areas: VLSI Design, Solid State Devices

Training Details

Training Title	Arrange By	Venue	Date	Level	Category	Type
IIT Spoken Tutorial Certification for LaTeX	SITS	Online	4 May, 2020	National	Faculty	External
Distance Learning for Educators by Google	Google	Online	6 May 2020	NA	Faculty	External
Logic Design and Computer Organization, Logic Design and Computer Organization Laboratory	SAE, Kondhwa	SAE, Kondhwa	30 June, 2020	University	Faculty	External
Final Year revised syllabus of VLSI Design & Technology	SPPU	SPPU	30 Jan 2018	University	Faculty	External
Third Year revised syllabus	SPPU	SPPU	14 - 16 Dec, 2017	University	Faculty	External
Third Year revised syllabus	SPPU	SPPU	11 - 13 Dec, 2014	University	Faculty	External
FPGA Development using Zinq Evaluation Board	COEP	COEP	14 - 16 Feb, 2013	University	Faculty	External
Design flow using ZedBoard Evaluation, Development Board and Xilinx Embedded Development Board	COEP	COEP	10 - 11 Jan, 2013	National	Faculty	External

Workshop Attended

Workshop	Topic	Conducted By	Date	Level	Category	Type
FDP	Teaching Tools and Design Thinking	Vel Tech Multi Tech Dr.Rangarajan Dr.Sakunthala Engineering College	22 April – 6 April, 2021	National	FDP	External
FDP	VLSI Physical Design using Cadence Innovus Tool	Dept. of ECE,St. Joseph's College of Engineering	20 – 24 July, 2020	National	FDP	External
FDP	Pioneering AI Technology- A Case Study Approach	Vemana Institute of Technology	22 – 26 June, 2020	National	FDP	External
FDP	Outcome Based Education: A Step Towards Excellence	Government College of Engineering, Karad	11 – 15 May, 2020	National	FDP	External
FDP	FDP on Research Methodology	RMDSSOE	20 – 25 May, 2020	National	FDP	External
FDP	Assessment of Pre-Final Year Engineering Students	International Institute of Information Technology	13 June 2020	National	FDP	External
FDP	Train the trainer on Digital Electronics and Logic design	SCOE	9 Sept, 2019	National	FDP	External
FDP	Assessment and Evaluation under Outcome Based Education (ICT04)	NITTTR, Kolkata	10 – 14 June, 2019	National	FDP	External
FDP	Research Trends, Techniques & Opportunities in Data & Text Analytics with Machine Learning	MMCOE	25 Feb – 1 March, 2019	National	FDP	External
FDP	Train the trainer in E&TC	SCOE	11 – 13 Dec, 2017	National	FDP	External
FDP	Workshop on 3-D Printer	SITS	14 – 15 Sept, 2017	National	FDP	External
FDP	Challenges in VLSI: FOSS in cloud solutions?	PVG COE	12 June, 2017	National	FDP	External
FDP	Train the trainer program for Electromagnetics	SCOE	5 – 10 June, 2017	National	FDP	External
FDP	Implementation of IOT & Image Processing using Raspberry Pi	VIT	11 – 14 Nov, 2016	National	FDP	External
FDP	CMOS IC Design using Empyrean	SCOE	29 – 30 June, 2015	National	FDP	External

Workshop	Topic	Conducted By	Date	Level	Category	Type
	Design Tool					
FDP	Challenges in ASIC design	VIIT	15 -19 June, 2015	National	FDP	External
FDP	Workshop on Disaster Management Training Program	SITS	11 -12 Feb, 2014	National	FDP	External
FDP	FDP for Electronics and Electrical Engineering	SKNCOE	13 Dec, 2013	National	FDP	External
FDP	Train the trainers at Mechanical Engineering	SKNCOE	9 – 14 Dec, 2013	National	FDP	External
FDP	Seminar on High Performance Computing	SKNCOE	9 – 10 Oct, 2013	National	FDP	External
FDP	Orientation meeting for online examination for the subject Electromagnetics	SPPU	14 March, 2013	National	FDP	External
FDP	NPTEL Awareness Program	SAE, Kondhwa	9 March, 2013	National	FDP	External
FDP	Methodology to make Electromagnetic Engineering and interesting subject	IETE, Pune	5 Feb, 2013	National	FDP	External
FDP	Workshop on Global Positioning System	SKNCOE	19 Jan, 2013	National	FDP	External
FDP	IETE Approves One Week STTP on Wavelets	IETE, Pune	13 -18 Dec, 2012	National	FDP	External
FDP	DSP Implementation on FPGA Platform	SCOE	20 – 30 June, 2012	National	FDP	External
FDP	Matlab, Simulink and System Generator – Matrix Reloaded	SCOE	18 – 19 Feb, 2012	National	FDP	External
FDP	FDP on Teaching Learning Process & Team Building	SCOE	21 – 23 Dec, 2011	National	FDP	External
FDP	Mathematical Methods and Techniques in Engineering Research	Trinity COE	20 – 22 Dec, 2011	National	FDP	External
FDP	State level FDP on Documentation with Latex	SITS	16 – 17 Nov, 2011	State	FDP	External
FDP	Workshop on	SPPU	13 July,	National	FDP	External

Workshop	Topic	Conducted By	Date	Level	Category	Type
	Syllabus detailing of Electronic Product Design		2011			
FDP	VLSI IC Design	VIIT	28 – 29 Oct, 2009	National	FDP	External
FDP	Global ICT Standardization Forum for India (GISFI)	SKNCOE	29 – 30 May, 2009	National	FDP	External
FDP	IEEE VLSI Design and Test Symposium VDAT	Wipro, Bangalore	23 – 26 July, 2008	National	FDP	External
FDP	Workshop on VLSI Design and Design Contest	VIIT	7 – 8 March, 2008	National	FDP	External
FDP	VLSI Design Program	SCOE	31 Jan – 2 Feb, 2008	National	FDP	External

Research Project

Project name	Description	Status	Budget	Project type
Early-Stage Detection of Microcalcifications in Mammograms using Wavelet/Curvelet Transform	Medical diagnostic support system for detection of microcalcification in mammograms	Completed	Rs. 2,40,000	BCUD Sponsored

Research Paper & Journal

ISSN/ ISBN No	Paper Title	Publish Date	Journal/Conference Name	Level	Paper Status	Authors
2233-7857	Deep learning based Diabetic Retinopathy	2021-08-13	International Journal of Future Generation Communication and Networking	International	Published	Chetan Bhamare, Vivek Niwane, Avni Bhatia, Shubham Gite
2233-7857	Solar Powered IOT based Smart Helmet	2021-08-18	International Journal of Future Generation Communication and Networking	International	Published	Mantri Onkar, Vivek Niwane, Jadhav Rohan, Takale Swapnil
978-1-5386-4288-7	Atman – Eternal Driver for Temporarius Soma	11 April 2019	IEEE Xplore	International	Published	Vivek Niwane; Hrishikesh Kulkarni

ISSN/ ISBN No	Paper Title	Publish Date	Journal/Confer ence Name	Level	Paper Status	Authors
	Modelling and Realization of New Technology Device, Classical CMOS Scaling: The End of the Road	23 Sept, 2015	AVISHKAR - 2015	State	Published	Vivek Niwane
	Impact of CMOS nanometer scaling	23 Dec, 2013	AVISHKAR - 2013	State	Published	Vivek Niwane
	Computation salient Bit Slice RISC Architecture	21 - 22 Oct, 2010	AVISHKAR - 2010	State	Published	Vivek Niwane
	Deep Submicron Implementation of CMOS RISC Processor	22 - 24 Dec, 2009	National Conference on Futuristic Computer Application	National	Published	Vivek Niwane
	VLSI Aware Implementation of RISC Processor	21 - 23 Dec, 2009	National Conference on Emerging Trends in Embedded Systems and VLSI	National	Published	Vivek Niwane
	Deep Submicron Implementation of A CMOS RISC Processor	25 April, 2009	ePGCON-2009	State	Published	Vivek Niwane

Declaration

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date: 03 March' 2022

Name: Vivek Prakash Niwane