Profile

1. Name: **Prof.Vikram M. Chavan.**
2. Designation: Asst. Professor, Computer Department
3. Institute: Sinhgad Institute of Technology, Lonavala
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5. Date of Joining : July 2, 2012
6. Mobile: +91-9970125757
7. Date of Birth: 04th July 1987
8. Address: NEW 2-BHK Building-A, Flat No.10, STES Campus , Lonavala
9. Research Interests: VLSI &Embedded system**.**

❖ **Educational Details:**
- **M.E**. (VLSI & Embedded system), SIT, Lonavala, Pune, First class, Dec 2012(Pune uni.).
- **B.E.** (E&Tc) from SVPM,s College of Engineering, Malegaon(Bk),Baramati, First class with distinction, June 2009(Pune Uni.)
- **H.S.C**.from, Kolhapur Board First class, April 2004.

Experience: (13 years):

2012 till date: - Working as a **Assistant Professor** in E/TC,Comp Dept, Sinhgad Institute of Technology,

Lonavala, India.

Academic Recognition:

✓ Recognized as UG Teacher from June 2017 to Till Date.
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Subjects Taught:
- Digital Electronics   Digital Electronics & Logic Design   Microprocessor
- Power Electronics   Basic Electronics Engg.   Embedded system design

Responsibilities Handled:
- Departmental theory internal Exam Co-ordinator from 2021.
- Sr.Supervisor, Assistant CEO for SPPU exam -2023-24.
- NAAC hospitality team coordinater August 2022.
- Event Co Co-ordinator –Techtonic 2024 VLogging &BLogging Workshop.

Research publications:
- o International Journals: **07**, National Conference: **02**

Achievements and Awards:

- Worked as a **Session Organizer in "ePGCon (2008)-14"** at SIT, Lonavala.(22th Nov 2014)

- Worked as **"Faculty co-ordinator of the Logistic Committee"** for the national level Technical conference "TECHTONIC-2014", at SIT Lonavala. (10th feb. 2014)

- Worked as "**Faculty Coordinator for Industrial Automation Training Workshop (PLC)",** at SIT Lonavala.(15 March 2015)

- Worked as a **Coordinator** for two day state level STTP on **"WSN fundamentals & protocol design using NS-2 & NS-3"**sponsored by University at SIT Lonavala.(23rd -24th Jan. 2016)

- **"Award of Excellence"** for excellent contribution in teaching Learning Process at Institute **A.Y.:2012-13. (5th sep. 2013)**

- Worked as **"Faculty co-ordinator of the Event 3D Rubiks Cube"** for the national level Technical conference "TECHTONIC-2019", at SIT Lonavala. (20-22th feb. 2019)

- Worked as **"Faculty Co co-ordinator of the Event VLogging & BLogging"** for the national level Technical conference "TECHTONIC-2024", at SIT Lonavala. (14-15th feb. 2024)

Place: Lonavala   **Sign**
                                                                                  (Prof Vikram M. Chavan)