

Profile

1. Name: **Dr. Dnyaneshwar S. Mantri**
2. Designation: Professor, Department of E&TC
3. Institute: Sinhgad Institute of Technology, Lonavala
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5. Date of Joining : July 3, 2006
6. Mobile: +91-9922431612
7. Date of Birth: 03rd Sept. 1970
8. Address: 2BHK, Flat No.09, STES Campus , Lonavala
9. Research Interests: **Wireless Sensor Networks, Wireless Networks , Adhoc Networks, VANAT**



Educational Details:

- Ph.D. (Wireless Communication), Aalborg University, Denmark, 2017
- M.E. (Electronics), Shivaji University, Kolhapur, First class (63.87%), 2006
- B.E. (Electronics), Shivaji University, Kolhapur, First class with Distinction (66%), 1992
- H.S.C (Pune Board), Walchand college of Arts and Science, Solapur, Distinction (81.67%), 1988
- S.S.C (Aurangabad Board), Janata Vidya Mandir, Murud, First class (72.28%), 1986

Experience: (27 years)

- 2018 to till date:-Professor, E&TC Dept., Sinhgad Institute of Technology, Lonavala, India
- 2010 to 2018: - Associate Professor E&TC Dept., Sinhgad Institute of Technology, Lonavala, India
- 2006 to 2010: - Assistant Professor E&TC Dept., Sinhgad Institute of Technology, Lonavala, India.
- 1998 to 2006:- Lecturer in Electronics and Telecommunication Engg. Dept., STBT, Tuljapur.
- 1996 to 1998:- Lecturer in Electronics and Telecommunication Engg. Dept., MGM's COE, Nanded,
- 1993 to 1996:- Lecturer in Electronics Dept. at M.C.E. Nilanga, MS .India

Academic Recognition

- ✓ Recognized as PG Teacher from April 20, 2011 to Till Date
- ✓ Recognized as Ph.D Research Guide under the faculty of Science and Technology in the subject of Electronics & Telecommunication Engineering for the period from 22-03-2021 to 21-03-2029, SPPU Pune.
- ✓ Recognized as Ph.D Research Guide under the faculty of Science and Technology in the subject of Computer and Information Technology for the period from 14-12-2022 to 02-09-2030, DOT SPPU Pune.

Subjects Taught:

- Microcontrollers and Applications
- Digital Electronics
- Mechatronics
- Wireless Sensor Network
- Advanced Processors / Embedded
- Information Theory and Coding Techniques
- Integrated Circuits
- Linear Integrated Circuits

Responsibilities Handled:

- Dean Academics, SIT, Lonavala
- NBA, NAAC Coordinator- Institute Level
- Chairman , Library Committee, SIT, Lonavala
- Member of IPR Committee – College Level
- Member of Research Review Committee- Department
- Convener, TKD-14 and TKD-20, TKD-22, TKD-23
- TPC In I2CT , IEEE Conference 2020-21
- General Co-chair for IEEE Conference GCWCN2014 and GCWCN2018
- NAAC Pear Team In-charge [2016-17]
- Head of E&TC Department. [2006, 2014-15]
- NBA Coordinator SIT, Lonavala [2009-2013]
- Member of Purchase committee [2007-2012]
- QAP Program In charge for the Department

- Chairman of FE admission Committee [2007,08,09]
- Coordinator for STTP and FDP on NS-2, NS-3 and Latex Workshop

Research publications:

- International Journals: **21**, National Journal: **06**, International Conference: **30**, National Conference: **04**, Book Chapters: **05**
- Google Citation :**308**, h-index: **08**, i10 index: **08** (As on 15th June.2023)

Conference Organized:

- ✓ TechnoKnocdown 2014,21,22,23,24 –IEEE and IETE Supported Student Conference
- ✓ 2014 Global Conferences on Wireless Computing and Networking (IEEE GCWCN2014)
- ✓ 2018 Global Conferences on Wireless Computing and Networking (IEEE GCWCN2018)
- ✓ IEEE 6th International Conference for Convergence in Technology 2020, Pune India
- ✓ SPPU University level PG Conference e-PGCON-2014

Professional Membership:

- ✚ Life Member of the International Society for Technical Education. (LMISTE 51293.)
- ✚ Fellow of IETE (F215186.)
- ✚ Senior Member of IEEE (92496577)

Books Published:

- 6G Connectivity-Systems, Technologies, and Applications, Digitalization of New Technologies, 6G and Evolution
- A Practical Approach to Electronic Circuit Design- Nikita Publication, Latur
- Microcontrollers : 8051 and PIC, In Assembly ISBN: 978-93-332-1575-6
- Microcontrollers Decode : 8051 and PIC, In Assembly ISBN: 978-93-332-1708-8
- Microcontrollers : 8051 and PIC, In C ISBN: 978-93-91567-60-6
- Microcontrollers Decode: 8051 and PIC, In C ISBN: 978-93-5585-134-5
- Proceeding of IEEE Conference GCWCN2014
- Proceeding of IEEE Conference GCWCN2018

Achievements and Awards:

- ❖ EC Member and Secretary, IETE Pune Local Center, for Duration (2024-2026)
- ❖ EC member and Vice Chair, IETE Pune Local Center, for Duration (2022-2024)
- ❖ Recognized as Ph.D Guide in SPPU University
- ❖ Session Chair, in reputed Conferences like IEEE, Springer, EPGCON etc
- ❖ Series Editor of E-book, STM Journals.
- ❖ Guest editor for Wireless Technology Journal
- ❖ Guest Editor For Special issue of GWS-2015, WPC Springer Journal
- ❖ Becomes TPC and Session chair at ICACCI2013, 2014 and 2015 conference., ICPC2015,
- ❖ Reviewer of renowned Journal and conferences, [Springer, Elsevier, IEEE, Sensor Journal, Pervasive computing Journal, Ad-hoc Networks, Athes- Journal]
- ❖ Subject Chairman and Paper setters of University of Pune, Solapur University and Shivaji University.
- ❖ Member of LIC Committee University of Pune.
- ❖ Elected as Board of Studies Member, Dr. BAMU Aurangabad

Place : Lonavala

Dr. D. S. MANTRI

Important Link

Google Scholar: <https://scholar.google.com/citations?user=pAx-Uz8AAAAJ&hl=en>

Linkedin: <https://www.linkedin.com/in/dnyaneshwar-mantri-7b0b29140/>

ORCID: [Dnyaneshwar Mantri \(0000-0001-6778-9024\)](https://orcid.org/0000-0001-6778-9024) - ORCID | Connecting Research and Researchers

Publon: <https://publons.com/researcher/1197463/dnyaneshwar-s-mantri/>

Web of Science Researcher ID: [AAA-9291-2021](https://www.researchgate.net/profile/Dnyaneshwar-Mantri)

Research Gate: <https://www.researchgate.net/profile/Dnyaneshwar-Mantri>