

- **Name** : Mrs. Manisha Vikramsinh Desai
- **Designation** : Assistant Professor
- **E-mail ID** : manishadesai@sinhgad.edu
- **Qualifications** : B.E.(CSE), M. Tech. (CSE)
- **Total Experience** : 8 Years
- **Subject Taught** : Digital Electronics & Logic Design, Database Management & System Administration, Microprocessor, High Performance Computing, Data Structure & Algorithm
- **Research Paper Published** :
 1. Sonal Joshi, Shradha Hirve, Aditi Deshmukh, Puja Borse, Manisha Desai (2017)" Detection of Spam Tweets by Using Machine Learning", International Journal of Advanced Research in Computer Science and Software Engineering, Vol.-7, Issue -4.
 2. Aivash Deshmukh, Manisha Desai (2015)." Corrupted Video Recovery Using CODEC Specifications", International Journal of Innovative Research in computer and Communication Engineering, Vol. 3, Issue -8.
 3. Swapna Kashid, Manisha Desai (2015)." Resilient Identity Crime Detection Using Malay Phonetic Coding", International Journal of Advanced Research in computer science and Software Engineering, Vol. 5, Issue -7.
 4. Priya B. Bhosale, M. V. Desai (2015)." Intrusion Detection System with Deceptive Virtual Host", International Journal of Innovative Research in computer and Communication Engineering, Vol. 3, Issue -7.
 5. Om Komawar, Prasad Thakar, Rohit Shetty, Akshay Bartakke, Manisha Desai (2015), "An Internet Relay Chat Bot Using AIML", International Journal of Science and Research, Vol. 4, Issue -10.
 6. Avinash Deshmukh , Manisha Desai (2014). "A Survey on Corrupted Video Recovery Using CODEC Specifications ", International Journal of Science and Research, Vol. 3, Issue -12.
 7. Priya B. Bhosale, Manisha V. Desai (2014)." Dynamic Honeypot Security with Deceptive Virtual Host", International Journal of Science and Research, Vol. 3, Issue -12.
 8. Prashant Pandit, Sagar Bhawar, Manisha Desai (2014)." Campus E-Voting for Android and Web Based Application", International Journal of Emerging Engineering Research and Technology, Vol. 2, Issue -7.
- **Paper Presented in International /National Conference** :
 1. Presented and published a paper for second international conference on Current Trends in Engineering and Technology (ICCTET) 2014, with a title "Data Security Approach for Online Social Network".
 2. Presented and published a paper for International Conference of Computer Science and Information Technology 2011, Bangalore named "Feature Selection using Various Algorithm for Speech Recognition".
 3. Presented and published a paper for First international conference on Computer Science, Engineering and Application 2011, Chennai with title "Speech Classification using Zernike Moments".

- **Seminar, Workshop, Training, FDP Attended :**
 1. Attended National Seminar on Relevance of Research in teaching and learning process in March 2012.
 2. Participated in the hands on workshop for Mathematical modelling using Maple in April 2012.
 3. Participated in Faculty Development Program on FPL –II in December 2012.
 4. Participated in DesignTech’s “High Performance Computing –Using PBS Pro, Altair Workshop” held between 11th April to 23rd April 2016.
 5. Participated as a Trainer for one unit of Digital Electronics and Logic Design in one Week “STES’s Train the Trainer Program” held from 17th to 22nd June 2013.
 6. Attended as a Subject Expert for Digital Electronics and Logic Design in one Week “Train the Trainer Program” (TTT) of S.E. (Computer Engineering) held from 2nd June to 7th June 2014.
 7. Attended as a Subject Expert for the subject MIT in 3 days “Train the Trainer Program” (TTT) of S.E. (Computer Engineering) held from 4th Dec to 6th Dec 2014.
 8. Participated in Short Term Training Program (STTP) on “Trends in Internet Technologies & its Applications” from 3rd to 9th January, 2017 at RMD Sinhgad Technical Institutes Campus
 9. Attended One day Faculty Development Program on “Principles of Programming Languages” (2015 course) on 16th December, 2016 in association with BOS Computer Engineering , SPPU organized by Department of Computer Engineering, MMCOE Pune.
 10. Attended One day Syllabus Implementation Workshop for “Microprocessor Laboratory” (2015 course) on 23rd December, 2016 in association with BoS Computer Engineering, SPPU organized by The Department of Computer Engineering, MESCOE (Wadia), Pune.

- **Professional Bodies Memberships :**
 1. Lifetime member of International Association of Engineers
 2. Lifetime member of International Society for Research & Development.