



Sinhgad Technical Education Society's

Sinhgad College of Engineering
Vadgaon (Budruk) Off. Sinhgad Road,S.No. 44/1,



Prof. Vijay Bhatu Baru

BE ENTC

vbbaru.scoe@sinhgad.edu

9822997441

Personal Details

Current Address	AS ABOVE
Permanent Address	G-10 FLAT NO.7 OMKAR GARDEN MANIKBAUG PUNE-411051
Marital Status	Married
Blood Group	O+
Telephone	+91-9822997441
Email Id	vbbaru.scoe@sinhgad.edu
Gender	Male
Date of Birth	Jun 18,1972
Strengths	sincere and hardwork
Weaknesses	very strict
PAN	ACRPB0106B
PassPort No.	-

Significant Achievement

- BE RESULT 100% APPRECIATION LETTER
- ME PROJECT EVALUATION EXPERT FOR ME PROJECT EVALUATION AT GHR IOET PUNE
- Reviewer for conference CASP-2016

Academic Qualification

Degree	Institute Name	University Name	Year of Passing	Percentage	Grade	Qualification Status
Master of Engineering	COE PUNE	Pune university	1999	60.0%	FIRST CLASS	Completed
BE Electronics and TeleCommunications	GOVT. COE PUNE	Pune university	1993	63.0%	FIRST CLASS	Completed
PhD	College of Engineering Pune-05	Savitribai phule pune university	-	-	-	Pursuing

Conference Details

Name	Venue	Date	Level	Category	Type	Sponsored By
SLOTTED PATCH ANTENNAS WITH EBG STRUCTURE FOR ISM BAND	Sardar Patel Institute of Technology	18 Oct 2012 to 20 Oct 2012	International	Research		TEQUIP
A REVIEW OF FAULT DIAGNOSIS IN DISTRIBUTED SYSTEMS USING COMPARISON MODEL	KBJ COE CHANDWAD	22 Feb 2013 to 24 Feb 2013	International	Research		--

PCB FAULT DETECTION USING IR THERMAL IMAGING	SCOE,VADGA ON,PUNE	15 Mar 2013 to 16 Mar 2013	National	Research		SELF
PCB FAULT DETECTION USING IR THERMAL IMAGING WITH OTSU BINARIZATION AND SUBTRACTION METHOD	MIT, ALANDI	04 Apr 2013 to 05 Apr 2013	National	Research		UOP
FAULT IDENTIFICATION IN PRINTED CIRCUIT BOARD USING THERMAL IMAGE	SCOE,PUNE	09 Apr 2010 to 10 Apr 2010	National	Research		SELF
FAULT IDENTIFICATION IN PRINTED CIRCUIT BOARD USING THERMAL IMAGE	ASM GROUP OF INSTITUTES, PUNE	12 Mar 2011 to 13 Mar 2011	International	Research		SELF
FAULT IDENTIFICATION IN PRINTED CIRCUIT BOARD USING THERMAL IMAGE	MIT,KOTHRUD	26 Apr 2011 to 26 Apr 2011	National	Research		UOP
THERMAL LEAKAGE DETECTION ON BUILDING FACADES USING TEXTURE EXTRACTION FROM INFRARED SEQUENCES	SAE, KONDHAWA	16 Mar 2012 to 18 Mar 2012	National	Research		UOP
APPLICATION OF RFID IN INTERACTIVE BOARD GAMES	CHANDWAD, NASHIK	22 Feb 2013 to 24 Feb 2013	International	Research		SELF
MULTINODAL SYSTEM FAULT DIAGNOSIS USING ANN	MIT,ALANDI	04 Apr 2013 to 05 Apr 2013	National	Research		UOP
STUDY OF VASCULAR IN THE FINGERTIP USING AN INFRARED THERMOGRAPHY METHOD	MIT,ALANDI	04 Apr 2013 to 05 Apr 2013	National	Research		UOP
THERMAL LEAKAGE DETECTION ON BUILDING FACADES USING INFRARED TEXTURE GENERATED BY MOBILE MAPPING	MIT,KOTHRUD	26 Apr 2011 to 26 Apr 2011	National	Research		UOP

ENERGY ROUTING PROTOCOL FOR WIRELESS SENSOR NETWORK	CUMMINS COLLEGE OF ENGG.,PUNE	22 Apr 2012 to 23 Apr 2012	National	Research		UOP
RECTANGULAR SLOTTED PATCH ANTENNAS WITH EBG	HYDRABAD	29 Apr 2012 to 30 Apr 2012	International	Research		SELF
Rectangular Slotted Patch Antennas with EBG	HYDRABAD	17 Apr 2013 to 18 Apr 2013	International	Research		SELF
Rectangular Slotted Patch Antennas with EBG	CCOEW PUNE	26 Apr 2012 to 26 Apr 2012	Institute	Intra-Institutional Conference		SELF
An Enhanced TCP Congestion Control Algorithm for Wiered/Wireless Networks	CiiT	10 Apr 2012 to 10 Apr 2012	International	Research		SELF
VLSI Implementation of Real-Time Image Segmentation	International Conference on IECR	08 Dec 2010 to 08 Dec 2010	International	Research		SELF
VLSI Implementation of Image Segmentation with Resource Optimized Adaptive Median Filter	IJCA Journal	09 Mar 2011 to 09 Mar 2011	International	Research		SELF
Key Frame Abstraction, Extraction and Browsing of Echocardiogram Videos	CiiT	11 Mar 2011 to 11 Mar 2011	International	Research		SELF
FPGA Based Image Segmentation	MIT PUNE	25 Apr 2011 to 26 Apr 2011	Institute	Intra-Institutional Conference		SELF
VLSI Implementation of Real-Time Image Segmentation	SCOE PUNE	09 Apr 2010 to 10 Apr 2010	National	Research		SELF
FPGA Based Implementation of Image Segmentation with Optimized Adaptive Median Filter	CiiT	15 Mar 2011 to 15 Mar 2011	International	Research		SELF
VLSI Implementation of Image Segmentation with Resource Optimized Adaptive Median Filter	International Conference on VLSI, Communication and Instrumentation	11 Mar 2011 to 11 Mar 2011	International	Research		SELF
FPGA Based Induction Motor Vector Control	MIT PUNE	25 Apr 2011 to 26 Apr 2011	Institute	Research		SELF
FPGA Implementation of UART with BIST Capability	JALGAON	17 Dec 2010 to 17 Dec 2010	National	Research		SELF

Measures to Improve Testing Problems in VLSI	SCOE	09 Apr 2010 to 10 Apr 2010	National	Research		SELF
FPGA Implementation of Universal Asynchronous Receiver/ Transmitter including Built in Self Test Capability	SCOE	25 Apr 2009 to 25 Apr 2009	Institute	Intra-Institutional Conference		SELF
Arbitrary Ratio Image Resizing using Fast DCT	SCOE	25 Apr 2009 to 25 Apr 2009	Institute	Intra-Institutional Conference		SELF
Finite Decimal Ratio Image Resizing??, National Conference on Advancement in Medical Instrumentation	THANE	23 Sep 2009 to 23 Sep 2009	National	Research		SELF
Bio-Informatics	AISSMS IOIT PUNE	17 Dec 2009 to 17 Dec 2009	National	Research		S
FPGA based Power Efficient Channeliser for Software Defined Radio	AASTHA	17 Oct 2009 to 17 Oct 2009	National	Research		S
Channeliser for Software Defined Radio	MUMBAI	12 Feb 2010 to 12 Feb 2010	International	Research		S
Software Defined Radio	SCOE	09 Apr 2010 to 10 Apr 2010	National	Research		S
Wireless Water Quality Monitoring System	M	20 Jan 2010 to 20 Jan 2010	National	Research		S
Intellectual Vehicle Data Logger as a Black Box	CCOEW PUNE	21 Jan 2010 to 23 Jan 2010	International	Research		S
FPGA based Channeliser for Software Defined Radio	SCOE	25 Apr 2009 to 25 Apr 2009	National	Intra-Institutional Conference		S
Comparison of JPEG Compression on Different Class Of Images	MUMBAI	18 Mar 2009 to 18 Mar 2009	National	Intra-Institutional Conference		S
Implementation of Wireless Enabled Gateway	MAE ALANDI	26 Dec 2007 to 27 Dec 2007	National	Research		S
HARDWARE SOFTWARE CODESIGN OF EMBEDDED SYSTEM USING FPGA	AISSMS IOIT PUNE	22 Dec 2009 to 24 Dec 2009	National	Research		Self

Research Projects

Project name	Description	Status	Budget	Prject type
Face and expression recognition analysis using LDN based on embedded tehnology		10	120000	Sponsored Projects

Face and Expression Recognition analysis using local directional number pattern	LDN extracts the local features from face textures and encodes it in compact code using and sign information which acts as a face descriptor.	In Process	0	Academic Projects
Multi-Scale Fusion: A new Approach for Image Dehazing	Effective approach is proposed to eliminate haze introduced due to bad atmospheric conditions.	In Process	0	Academic Projects
Vascular Reactivity in the Fingertip using an Infrared Thermography	An infrared thermography method is used to investigate the variation of fingertip temperature during VR test.	In Process	0	Academic Projects
FPGA Based controller for Car Black box using SOC	This project describes the Embedded controller for car black box using SOC technique.	Completed	0	Academic Projects
leakage detection on building facade using thermal image	The goal of this research is to detect leakage on building facade using thermal image. The project also focuses on such areas where this technology has a wide scope in the commercial application and one of such applications is roofing.	Completed	0	Academic Projects
PCB Fault Detection Using IR Thermal Imaging	To analyze the thermal image of PCB for fault detection a novel method to analyze the thermal image of PCB for fault detection is proposed.	Completed	0	Academic Projects
Multimodal system fault diagnosis using artificial neural network	Multimodal system fault diagnosis deals with the fault identification problem in multicomp and multiprocessor systems using Back Propagation Neural Network approach.	Completed	0	Academic Projects
An Efficient Synchronization scheme in cognitive radio system	Several aspects in cognitive radio for analyzing equalization and cognitive system are analyzed and estimation of frequency and offset estimation is done.	Completed	0	Academic Projects
RFID based interactive board games	This project aims to design an electronic chess board that guides the user to play a game which keeps track of the moves and timings of both sides in competition mode.	Completed	0	Academic Projects
A novel approach to address TCP congestion Problem	TCP Westwood algorithm is proposed to enhance the performance of the network. This algorithm is end-to-end in nature and modifies only the sender's algorithm.	Completed	0	Academic Projects

Fault identification in PCB using Thermal Image	Basic aim is to analyze the thermal image of PCB for fault detection proposed a novel vector quantization based approach.	Completed	0	Academic Projects
FPGA implementation of image segmentation	A pipelined implementation for image segmentation on FPGA is designed. A 24bit VGA DAC has been designed to display segmented images	Completed	0	Academic Projects
FPGA based vector control A.C induction motor	The purpose of this project is to design, simulate and implement the most feasible induction motor control algorithm for use in an electric vehicle applications	Completed	0	Academic Projects
FPGA implementation of Universal asynchronous receiver/transmitter with built in self test capability	This technique can provide shorter test time compared to an externally applied test and allow the use of low cost test equipments during all stages of production	Completed	0	Academic Projects
FPGA Based power efficient channelizer for software defined radio	This project describes the process of channelization as it applies to low power and high efficiency applications in wireless and satellite communications domains	Completed	0	Academic Projects
Arbitrary ratio images resizing using DCT	The proposed method combines fast inverse and forward DCT of composite length for arbitrary ratio upsizing or downsizing	Completed	0	Academic Projects
Efficacy of image compression for different class of images	This work relates to an implementation of JPEG & JPEG 2000 standard and focuses mainly on the comparison on different class of digital images	Completed	0	Academic Projects

Research Papers and Journals

ISSN/ISBN No	Paper Title	Publish Date	Journal/Conference Name	Level	Paper Status	Authors
ISSN: 2231-5381	PERFORMANCE OF ETHERNET PROTOCOL CONVERSION ON AN ARM-7 EMBEDDED PROCESSOR	2012	International Journal of Engineering Trends and Technology	Refereed and Indexed Journal	Accepted and Published	Vijay Baru
0974-973X	Fault Identification in Printed Circuit Board using Thermal Image	2012	CiiT International Journal of Programmable device circuits and systems Vol 4, No 7, JUNE 2012	Refereed Journal	Accepted and Published	Vijay Baru

0974-9756	energy efficient routing protocol for wireless sensor network	2012	CiiT International Journal of wireless communication , Vol 4, No 8, March 2012	Refereed Journal	Accepted and Published	Vijay Baru
2278-8875	Cache Controller with Enhanced Features using Verilog HDL	2015	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering	Journal - International level	Accepted and Published	Vijay Baru
2319-7064	Implementation of Enhanced Cache Controller with Multisized outputs	2015	International Journal of Science and Research, Vol. 4	Journal - International level	Accepted and Published	Vijay Baru
2278-8875	High Speed 16 Bit Digital Multiplier Architecture using Urdhwa Tiryakbhyam and Compressors	2015	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol.4	Journal - International level	Accepted and Published	Vijay Baru
2319-7064	Design of high Speed 32 Bit Multiplier Architecture using Vedic Mathematics and Compressors	2015	International Journal of Science and Research, Vol.4	Journal - International level	Accepted and Published	Vijay Baru
2320-883X	Super-Resolution of Image using Discrete Wavelet Transform	2015	A R Digitech International Journal of Engineering Education and Technology, Vol.3	Journal - International level	Accepted and Published	Vijay Baru
2319-7064	Conversion of Low Resolution Video into High Resolution Video using Super-resolution in Wavelet Domain	2015	International Journal of Science and Research, Vol.4	Journal - International level	Accepted and Published	Vijay Baru
2278-0181	Heart Sound Segmentation using Empirical Mode Decomposition	2015	International Journal of Engineering Research and Technology, Vol.4	Journal - International level	Accepted and Published	Vijay Baru
-	Review on Implementation of FIR Adaptive Filter using Distributed Arithmetic and Block LMS Algorithm	2014	International Journal of Modern Engineering Research	Journal - International level	Accepted and Published	Vijay Baru
2319-7064	HEART VALVE DISEASE CLASSIFICATION USING NEURAL NETWORK	2015	INTERNATIONAL JOURNAL OF SCIENCE AND RESEARCH(IJ SR), june 2015, volume 4 ISSUE 6	Journal - International level	Accepted and Published	Vijay Baru

2395-1621	IMPLEMENTATION OF MANAGEMENT INFORMATION SYSTEM IN MANUFACTURING INDUSTRY	2015	INTERNATIONAL JOURNAL OF ENGINEERING RESEARCH, SPECIAL ISSUE 2, PAGES 9-14	Journal - International level	Accepted and Published	Vijay Baru
2278-909x	PERFORMANCE ANALYSIS OF TEXTURE BASED FEATURE EXTRACTION TECHNIQUES FOR FACIAL EXPRESSION RECOGNITION	2016	INTERNATIONAL JOURNAL OF ADVANCED RESEARCH IN ELECTRONICS AND COMMUNICATION ENGINEERING (IJARECE), JUNE 2016, VOLUME 5, ISSUE 6	Journal - International level	Accepted and Published	Vijay Baru
2347-6710	UNFOCUSED RAINDROP AND FOG DETECTION	2016	INTERNATIONAL JOURNAL OF INNOVATIVE RESEARCH IN SCIENCE ENGINEERING AND TECHNOLOGY, JUNE 2016, VOLUME 5, ISSUE 6, PAGES 11808-11817	Journal - International level	Accepted and Published	Vijay Baru
2320-9798	LOCAL DIRECTIONAL NUMBER PATTERN: AN EFFICIENT DESCRIPTOR FOR FACE ANALYSIS	2016	International Journal of Innovative Research in Computer and Communication Engineering, June 2016, volume 4, issue 6, pages 1164-11672	Journal - International level	Accepted and Published	Vijay Baru
2320-3765	Cache Controller with Enhanced Features using Verilog HDL	2015	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, (IJAREEIE) vol 4, issue 2, February 2015	Journal - International level	Accepted and Published	Vijay Baru
-	Implementation of Enhanced Cache Controller with Multi-sized Outputs	2015	International Journal of Science and Research", vol. 4, issue 6, June 2015	Journal - International level	Accepted and Published	Vijay Baru
-	Implementation and Comparison of Texture Based Feature Extraction Algorithms for Facial Expression Recognition	2016	-	Conference-National level	Accepted and Published	Vijay Baru

-	Face and Expression Recognition Analysis using Local Directional Number Pattern Based on Embedded Technology	2016	e-PGPex Conference	Conference-National level	Accepted and Published	Vijay Baru
-	Unfocused Raindrop and Fog Detection	2016	e-PGPex Conference on Electronics and Telecommunication	Conference-National level	Accepted and Published	Vijay Baru
-	Safety Mechatronics System for Farming of Fruits and Vegetables	2016	Post Graduate Conference, D. Y. Patil College of Engineering, Pimpri	Conference-National level	Accepted and Published	Vijay Baru
2349-784X	Edge Responsive Local Directional Patterns versus Intensity Dependent Local Patterns: Implementation and Comparative Analysis	2016	International Journal of Science Technology and Engineering, vol. 3, issue 02, August 2016	Journal - International level	Accepted and Published	Vijay Baru
2321-8134	LOCAL DIRECTIONAL NUMBER PATTERN FOR FACE RECOGNITION	2016	IJFEAT, 2016	Journal - International level	Accepted and Published	Vijay Baru
-	UNFOCUSSED RAINDROP DETECTION USING SEGMENTATION AND FOG REMOVAL USING DEFOGGING ALGORITHM	2016	IJIRCE	Journal - International level	Accepted	Vijay Baru

Membership Details

Membership Body	Details	Duration	Level
ISTE	LM22571	01 Jul 2014 to 30 Jun 2015	National
IETE	M165661	01 Jul 2014 to 30 Jun 2015	National

Post Held

Organization Name	Post	Organization Category	Appointment Status	Duration
Sinhgad college of Engineering, Vadgaon (BK) Pune-41	Assistant professor	SINHGAD INSTITUTE CAMPUS	Full Time/Permanent	Dec 15,2000 to Dec 31,2005
AISSMS'S college of Engineering, Pune-411001	Lecturer	EDUCATIONAL INSTITUTE	Full Time/Permanent	Jul 20,1995 to Dec 14,2000
Govt. polytechnic, Nagpur-1	Lecturer	Government	Adhoc	Feb 01,1994 to May 31,1994
SCOE	ASSO PROF	SINHGAD INSTITUTE CAMPUS	Full time and Permanent	Jan 01,2006 to

Books Published

ISBN	Title	Publisher	Publish Year
9788126533169	Electronics Product Design	WILEY	Sept 2011

978-93-5004-012-6	BASIC ELECTRONICS ENGG. with CD	DREAMTECH	Sept 2010
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Self Contribution

Organizational Development Contribution(2012-2013)

Date	Venue	Contribution	Category
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Institutional Development Contribution(2012-2013)

Date	Venue	Contribution	Category
01,Jul - 30,Jun	SCOE	worked as adviser and chief coordinator	Alumni Coordinator
01,Jul - 30,Jun	SCOE	TO carry out different activities of ERP work	ERP COORDINATOR

Institutional Development Contribution(2010-2011)

Date	Venue	Contribution	Category
01,Jul - 30,Jun	SCOE	Worked as chief coordinator	Alumni Coordinator

Academic Development Contribution(2012-2013)

Date	Venue	Contribution	Category
01,Jul - 30,Jun	DEPT OF E/TC	To carry out different activities related to project	project coordinator
01,Jul - 30,Jun	dept	student's counseling, monitoring etc	Teacher Guardian
01,Jul - 30,Jun	DEPT	TO COORDINATE DIFFERENT ACTIVITIES RELATED TO PG SEMINAR	PG SEMINAR COORDINATOR

Academic Development Contribution(2011-2012)

Date	Venue	Contribution	Category
01,Jul - 30,Jun	DEPT	PLANNING FOR DIFFERENT ACTIVITIES RELATED TO PROJECTS FOR UG STUDENTS	project coordinator

Organizational Development Contribution(2011-2012)

Date	Venue	Contribution	Category
01,Jul - 31,Aug	STES OFFICE	TO COORDINATE THE VARIOUS ACTIVITIES	Staff Selection

Organizational Development Contribution(2010-2011)

Date	Venue	Contribution	Category
01,Jul - 29,Aug	STES OFFICE	TO COORDINATE THE VARIOUS TASK	Staff Selection

Organizational Development Contribution(2010-2011)

Date	Venue	Contribution	Category
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Student Development Contribution(2012-2013)

Date	Venue	Contribution	Category
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Academic Development Contribution(2010-2011)

Date	Venue	Contribution	Category
01,Jul - 30,Jun	DEPT	TO COORDINATE DIFFERENT ACTIVITIES	project coordinator

Institutional Development Contribution(2014-2015)

Date	Venue	Contribution	Category
01,Jun - 30,Jun	SCOE	ERP COMMITTEE MEMBER	Member of Committee

Academic Development Contribution(2013-2014)			
Date	Venue	Contribution	Category
01,Jun - 30,Jun	DEPT	To carry out all the activities related to projects	project coordinator
01,Jun - 30,Jun	dept	various task related to TG	Teacher Guardian

Academic Development Contribution(2014-2015)			
Date	Venue	Contribution	Category
01,Jun - 31,Jul	dept	to carry out all activities related to the UG projects	Project Co-ordinator
01,Jun - 30,Jul	DEPT	class coordinator related activities	Class Coordinator
01,Jun - 16,Jul	dept	TG various activities	Teacher Guardian

Declaration

I hereby declare that all the above information furnished by me are true to the best of my knowledge.

Date :13-Oct-2016

Prof. Vijay Baru