Curriculum Vitae

Mr. Rajesh U. Shekokar

Academic Qualifications

Bachelor's Degree in Engg: Industrial Electronics (2001)
College: Anuradha Engg.College Chikhli. Buldhana
University: Sant Gadge Baba Amravati University, Amravati

Master's Degree in Engg. Electronics Engg. (2012)
College: MGM'S JNEC Aurangabad

University: Dr. Babasaheb Ambedkar Marathwada University, Aurangabad.
Dissertation: FPGA Based Data Acquisition and Control System

Work Experience (10 Years UGC App.)

From July 2002 to December 2004
Worked as a Lecturer in Electronics Dept. , Mahrashtra Academy of Engg. Alandi, Pune.

From January 2005 to June 2013
Working as an Assistant Professor in E&TC Engg. Dept. in Genba Sopanrao
Moze C.O.E. Balewadi Pune.

From July 2013 to Till Date
Working as an Assistant Professor in E&TC Engg. Dept. in RMD Sinhgad School
of Engg. Warje Pune.

Subjects Taught

VLSI Design, Optical fiber communication and Digital Electronics, Digital Signal Processing,
Computer Network, Analog Integrated Circuits & Design, Basic Electronics Engineering, Solid State

Devices, DDCO, Electronics Circuits & Applications for E&TC and Electronics curriculum of Pune University.

Papers published
International Journal: 01
National conference: 02

Responsibilities Undertaken
1) Worked as External & Internal Senior Supervisor for Pune University Theory Examinations.
2) Worked as Custodian for Pune University Theory Examinations.
3) Worked as External/Internal examiner at Pune University Practical examinations.
4) Worked as Subject chairman, Examiner for Paper evaluation/paper setting.
5) Coorinator of two days workshop on "Embedded System Design & its Application" organized by E & TC Engineering Department, G. S. Moze College of Engineering, Balewadi, Pune – 45 & sponsored by University of Pune on date 3rd & 4th March 2011.
6) Conducted MBA, MCA and AIEEE CET exams.
7) Conducted Robotics workshop (uc8051 based) for SE students with PCB design and programming.
8) Worked as a Project Coordinator for E&TC Engg BE projects for 6 Years.
9) Guided more than 25 UG projects.
10) Coordinator for Line Follower Robot and Project competition Event in AURRA-11/12.

Software Skill
Operating Systems: Windows XP and Windows 2000.
Languages & Softwares: VHDL, Xilinx, Microwind, Keil, Matlab, Protus and Diptrace.

Field of Interest: Digital Design using FPGA, FIL and Video Processing using PLDs

Membership of Professional Body: ISTE (LMISTE - 37138)